

# Reversible Logic Elements with Memory

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1. What are reversible logic elements with memory (RLEMs)?
2. How can we construct reversible machines by RLEMs?
3. Can RLEMs be implemented in a reversible physical system efficiently?
4. Which RLEM is universal, and which is not?

**1. What are reversible logic elements with memory (RLEM)s?**

# 1. What are reversible logic elements with memory (RLEM)s?

**Answer** (at first, it is stated very shortly):

- They are very interesting logic elements as well as reversible logic gates (at least I think so).

# Design methods of logic circuits/systems

- Traditional method:  
Elements for logical operation (logic gates), and those for memory (flip-flops) are separated.
  - Also in the case of reversible logic circuits, this method has been mainly employed.
- Method of using logic elements with memory:
  - In asynchronous logic circuits  
E.g., [Keller, 1974], [Büning, Priese, 1980], etc.
  - In cellular automata (CAs)  
Each cell is a logic element with memory
  - In reversible logic circuits  
E.g., [Morita, 2001]

## Two kinds of reversible logic elements

### (1) Reversible logic elements without memory (i.e., reversible logic gates):

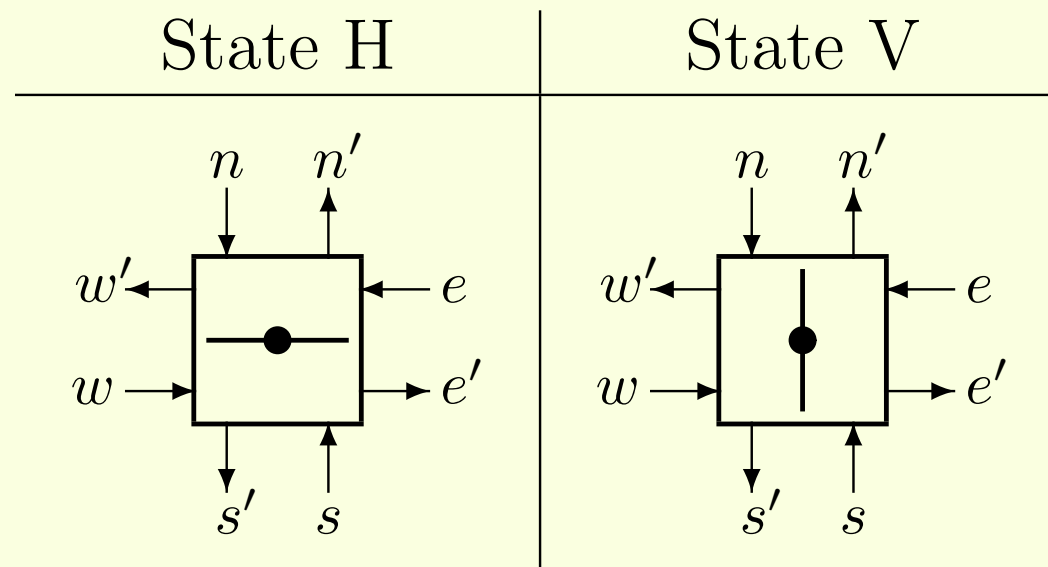
- Toffoli gate [Toffoli, 1980]
- Fredkin gate [Fredkin and Toffoli, 1982]
- etc.

### (2) Reversible logic elements with memory (RLEMs):

- Rotary element (RE) [Morita, 2001]
- $m$ -state  $n$ -symbol RLEM (in general)

## Rotary element (RE) [Morita, 2001]

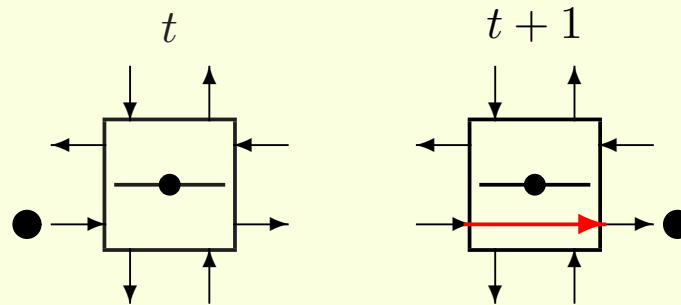
- A **typical** reversible logic element with memory.
- A 2-state 4-input-line 4-output-line element.
- Conceptually, it has a rotatable bar.



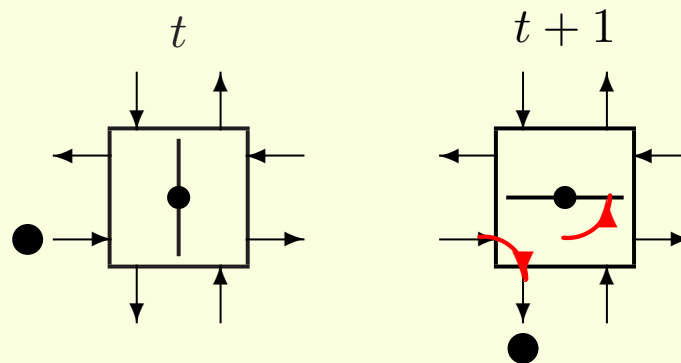
- Its operations are very easy to understand.

# Operations of a rotary element (RE)

- The bar in the box controls an incoming signal.
- Parallel case:



- Orthogonal case:



- Assume signal 1 is given at most one input line.
- An RE is a kind of **reversible sequential machine**.



# Reversible sequential machine (RSM)

$$M = (Q, \Sigma, \Gamma, \delta)$$

$Q$  : a set of states

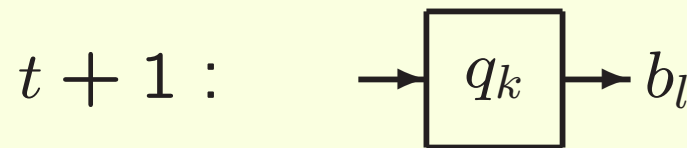
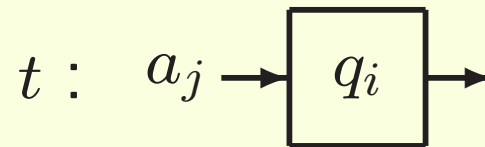
$\Sigma$  : a set of input symbols

$\Gamma$  : a set of output symbols

$\delta : Q \times \Sigma \rightarrow Q \times \Gamma$  : a move function

present state      input      next state      output

$$\delta(q_i, a_j) = (q_k, b_l)$$



- $M$  is called an **RSM**, if  $\delta$  is one-to-one.

## Rotary element is formalized as an RSM

$$M_{RE} = ( \underbrace{\{ \square, \square \}}_{\text{internal states}}, \underbrace{\{n, e, s, w\}}_{\text{input symbols}}, \underbrace{\{n', e', s', w'\}}_{\text{output symbols}}, \underbrace{\delta_{RE}}_{\text{move function}} )$$

- The move function  $\delta_{RE}$ :

Present state	Input			
	$n$	$e$	$s$	$w$
State H: $\square$	$\square w'$	$\square w'$	$\square e'$	$\square e'$
State V: $\square$	$\square s'$	$\square n'$	$\square n'$	$\square s'$

- From the next state and the output, we can determine the previous state and the input uniquely.

## **2. How can we construct reversible machines by RLEMs?**

## 2. How can we construct reversible machines by RLEMs?

**Answer** (though it is difficult to state in one line):

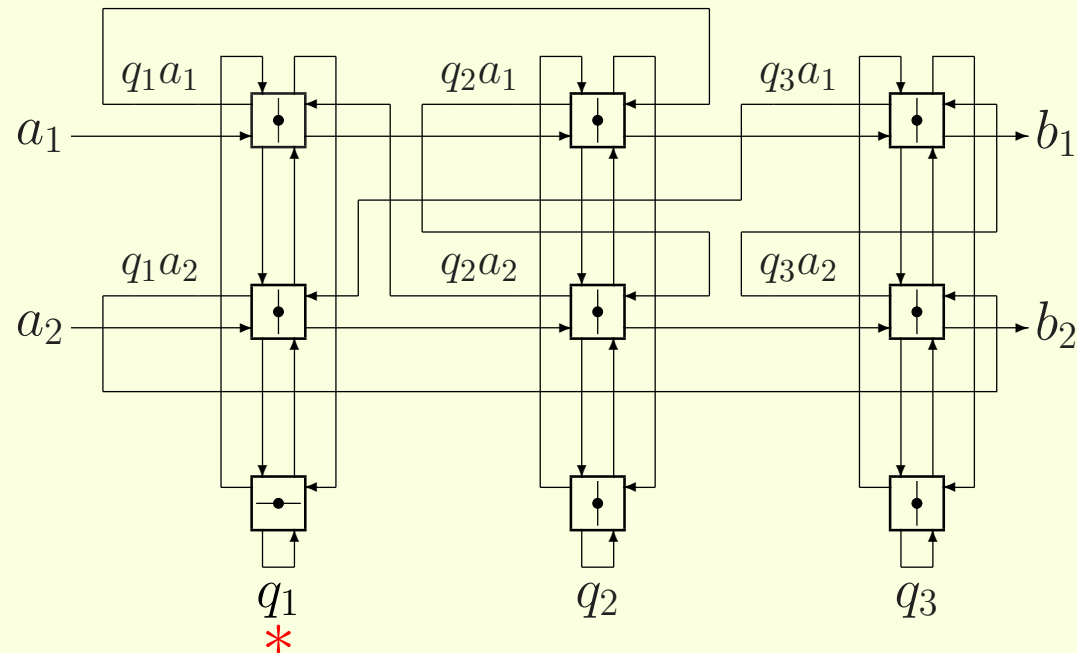
- They can be constructed very simply.

# Reversible sequential machine (RSM)

- We can construct any RSM by REs easily.

**Example:**

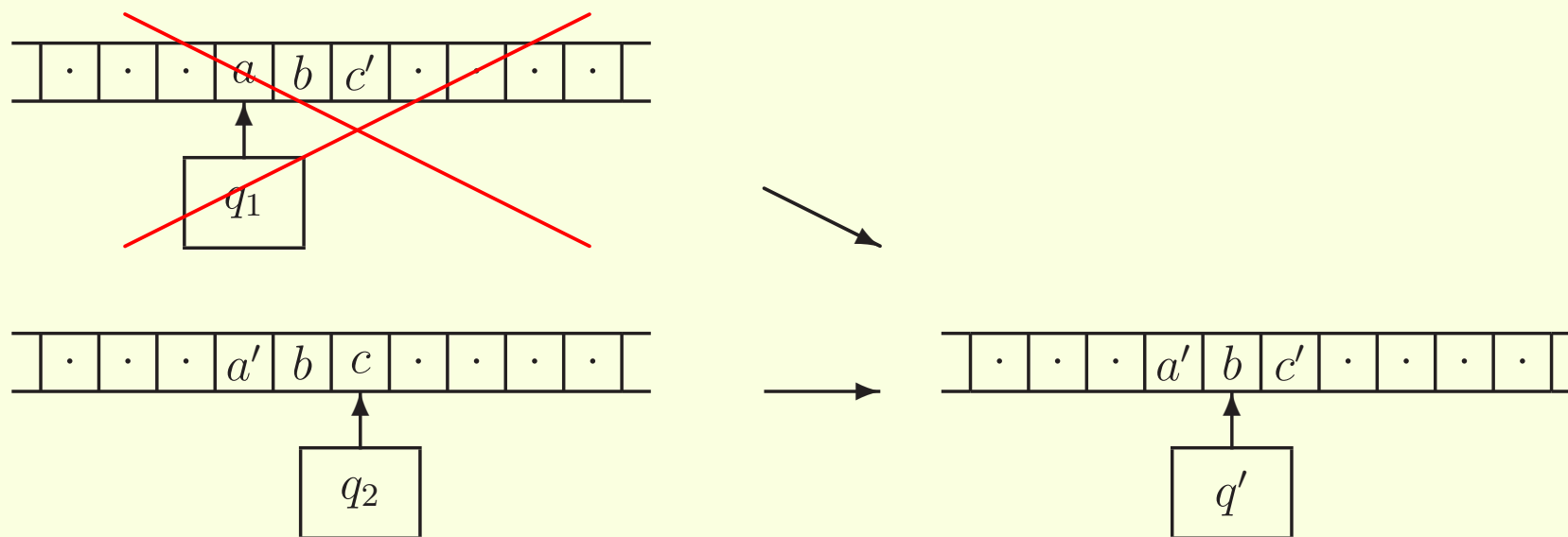
Input	State		
	$q_1$	$q_2$	$q_3$
$a_1$	$q_2$ $b_1$	$q_2$ $b_2$	$q_1$ $b_2$
$a_2$	$q_3$ $b_2$	$q_1$ $b_1$	$q_3$ $b_1$



[Morita, 2003]

## Reversible Turing Machines (RTMs)

- A “backward deterministic” TM.



- We can also construct any RTM by REs simply.

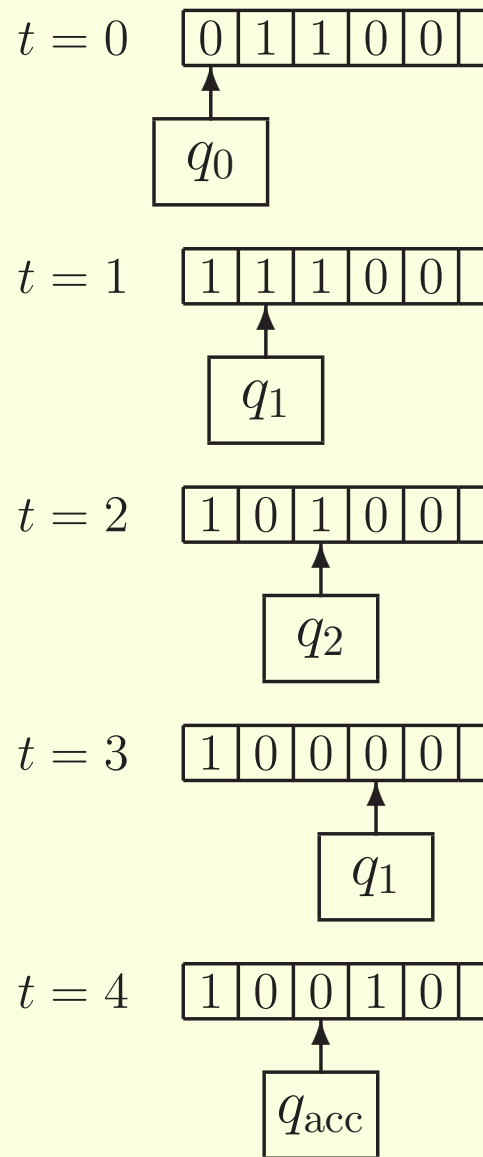
## A Simple Example of an RTM: $T_{\text{parity}}$

$$T_{\text{parity}} = (Q, S, q_0, \{q_{\text{acc}}\}, 0, \delta)$$

$Q = \{q_0, q_1, q_2, q_{\text{acc}}, q_{\text{rej}}\}$  : a set of states  
 $S = \{0, 1\}$  : a set of tape symbols  
 $\delta = \{ [ q_0, 0, 1, R, q_1 ], [ q_1, 0, 1, L, q_{\text{acc}} ],$   
     $[ q_1, 1, 0, R, q_2 ], [ q_2, 0, 1, L, q_{\text{rej}} ],$   
     $[ q_2, 1, 0, R, q_1 ] \}$  : a move function

- A unary expression of an integer  $n$  is given.
- If  $n$  is even,  $T_{\text{parity}}$  halts in the final state  $q_{\text{acc}}$ .
- If  $n$  is odd,  $T_{\text{parity}}$  halts in the state  $q_{\text{rej}}$ .
- All the symbols read by  $T_{\text{parity}}$  are complemented.

# A computing process of $T_{\text{parity}}$ for input "11"



$$\delta = \{ [ q_0, 0, 1, R, q_1 ], [ q_1, 0, 1, L, q_{\text{acc}} ], [ q_1, 1, 0, R, q_2 ], [ q_2, 0, 1, L, q_{\text{rej}} ], [ q_2, 1, 0, R, q_1 ] \}$$

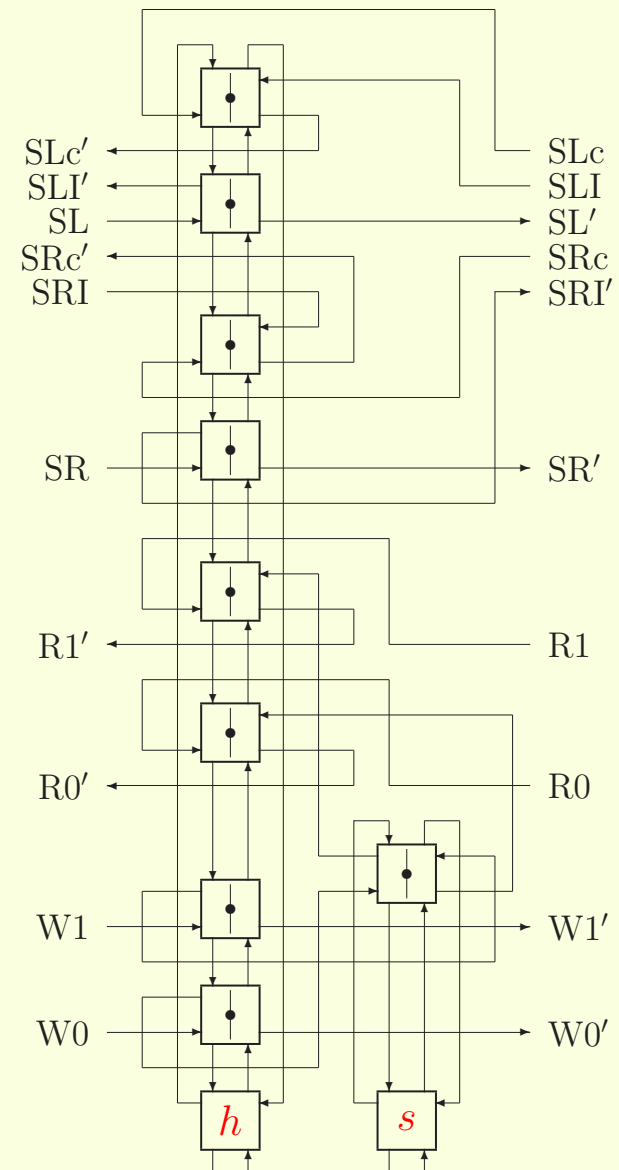


## Realizing RTMs by REs

- A **memory cell** (i.e., a tape square), and a **finite-state control** of an RTM are formalized as RSMs.
- Therefore, they are constructed by REs systematically as shown above.
- Here we use additional techniques to reduce the number of REs in them. [Morita, 2001, 2010]

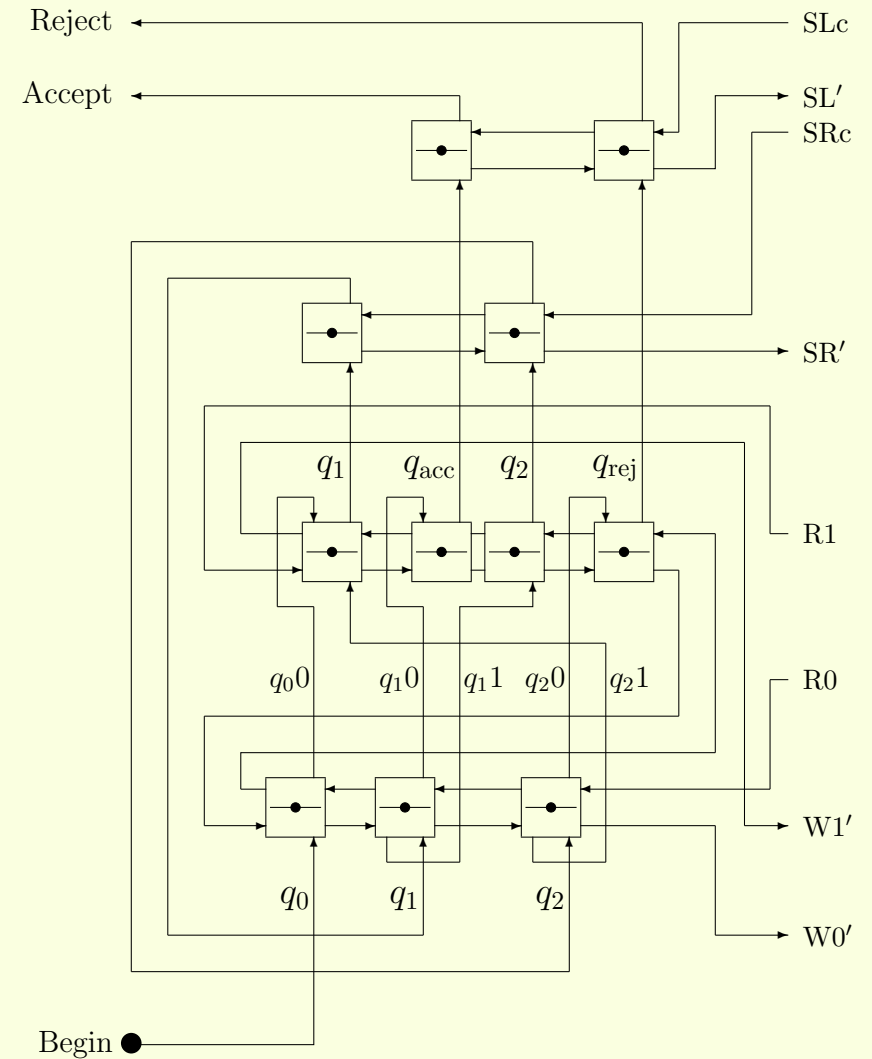
# Memory cell realized by REs

- It keeps a tape symbol  $s \in \{0, 1\}$ .
- It also keeps the information  $h$  whether the head is on this cell ( $h = 1$ ) or not ( $h = 0$ ).
- The REs at the positions  $h$  and  $s$  are set to  $\boxed{\bullet}$  if the value is 0, and  $\boxed{-\bullet}$  if it is 1.

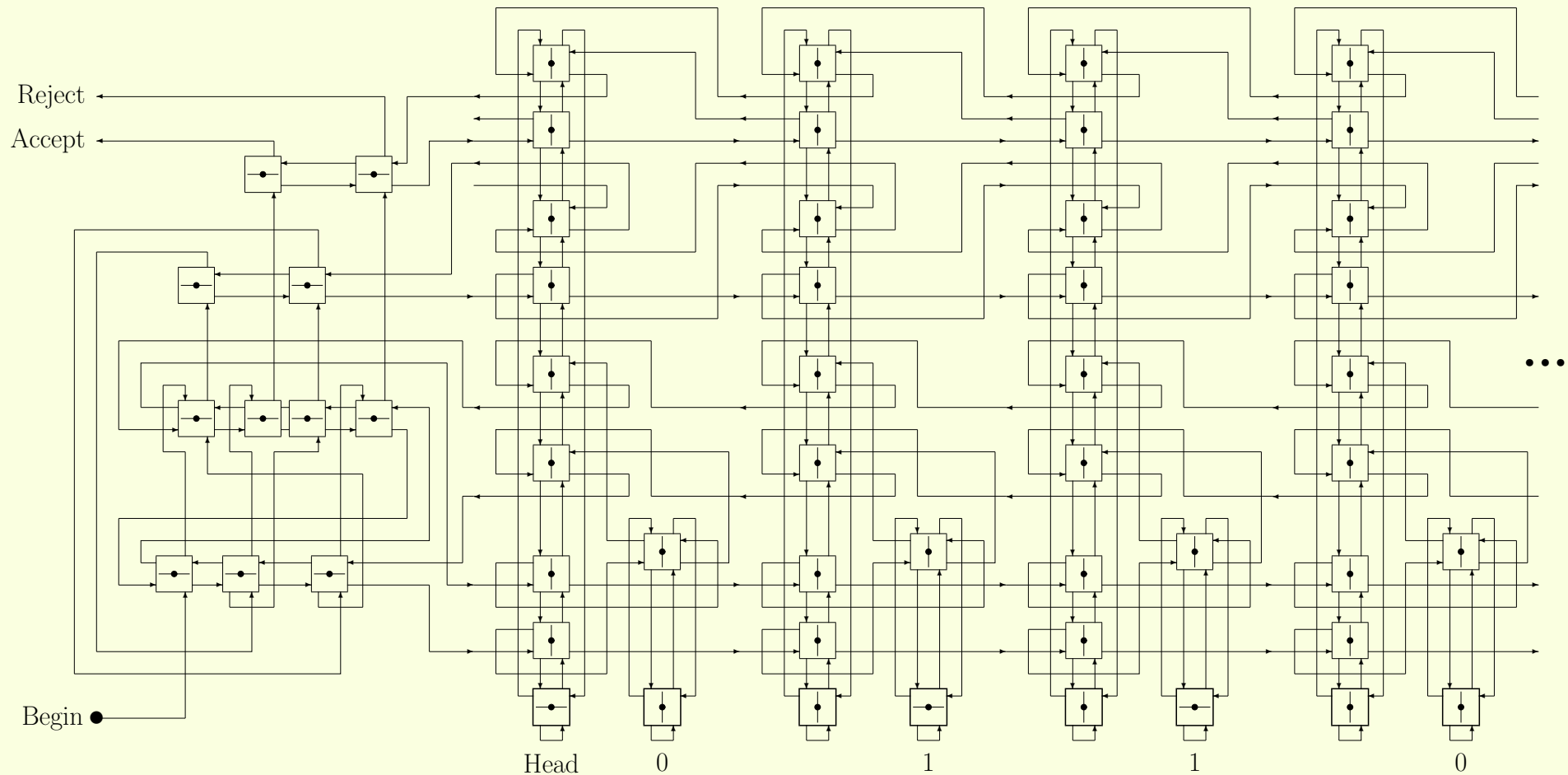


# Finite-state control of $T_{\text{parity}}$ realized by REs

- The three REs in the 4th row correspond to  $q_0, q_1$  and  $q_2$ . They read the symbol currently scanned.
- Then, the four REs of the 3rd row perform writing and state-change.
- Finally, the REs of the 1st or 2nd rows perform a head shift.



# The whole RE circuit that simulates $T_{\text{parity}}$



- Giving a particle to “Begin,” it starts to compute.
- It is also possible to further realize this circuit in BBM.

**3. Can RLEMs be implemented in a reversible physical system efficiently?**

### 3. Can RLEMs be implemented in a reversible physical system efficiently?

**Answer** (from my present knowledge):

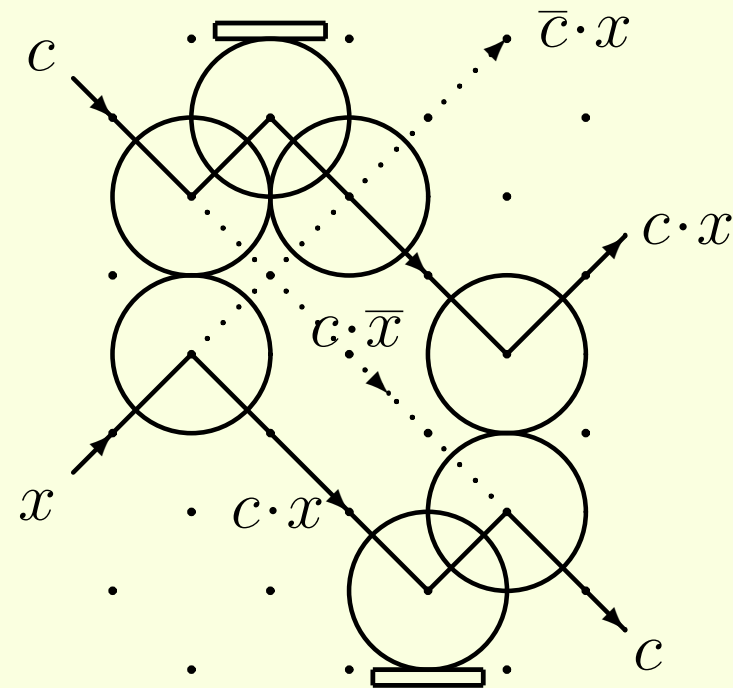
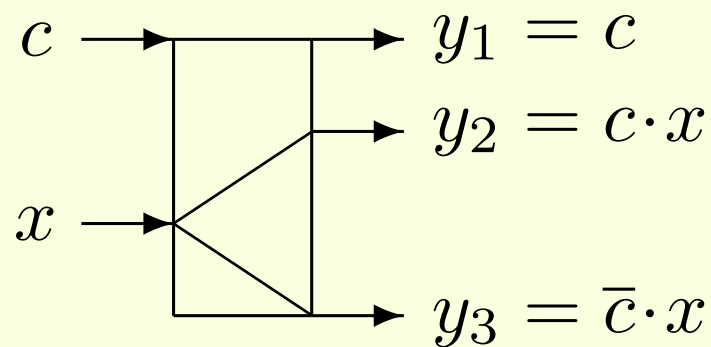
- “Yes,” by a thought experiment.
- “Unknown,” in a practical level.

## Billiard ball model (BBM)

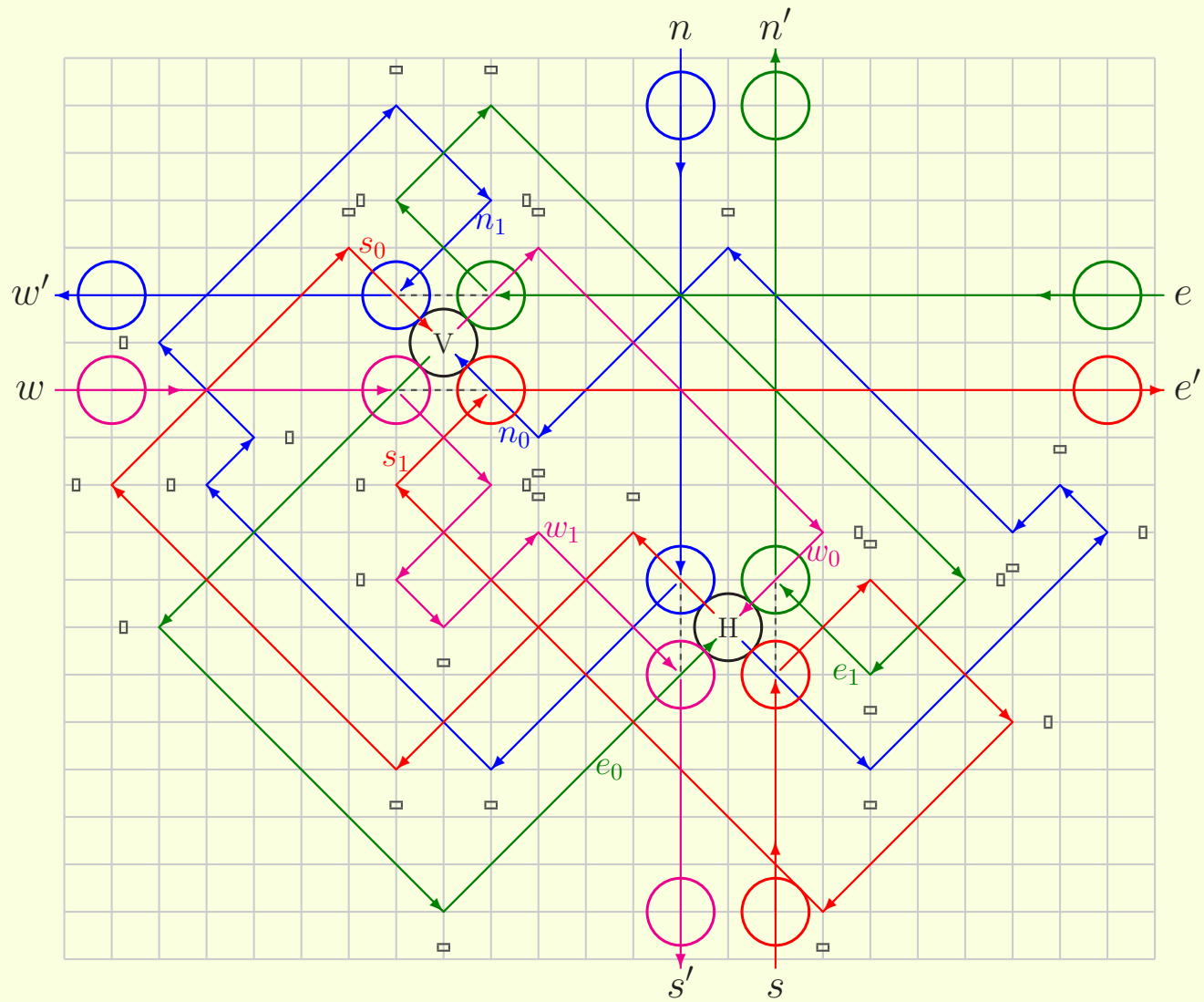
- A reversible physical model of computing.

[Fredkin and Toffoli, 1982]

- A switch gate is realized in the BBM.



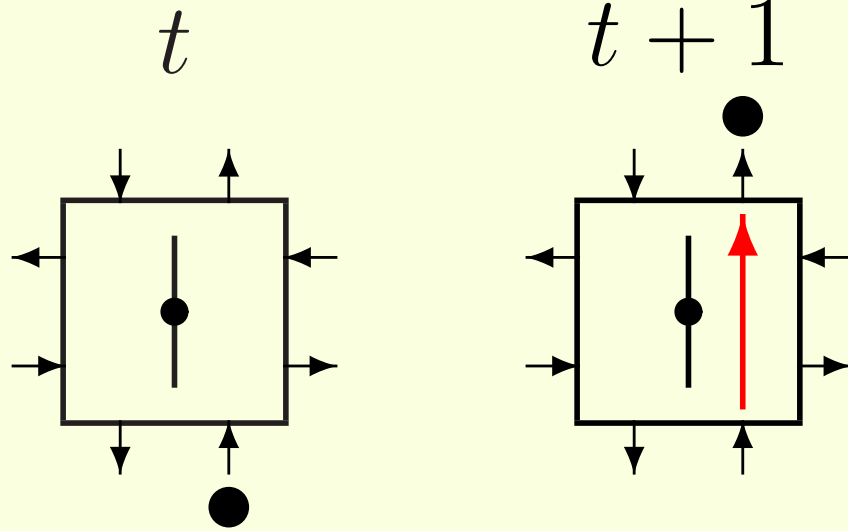
# Realization of an RE by BBM



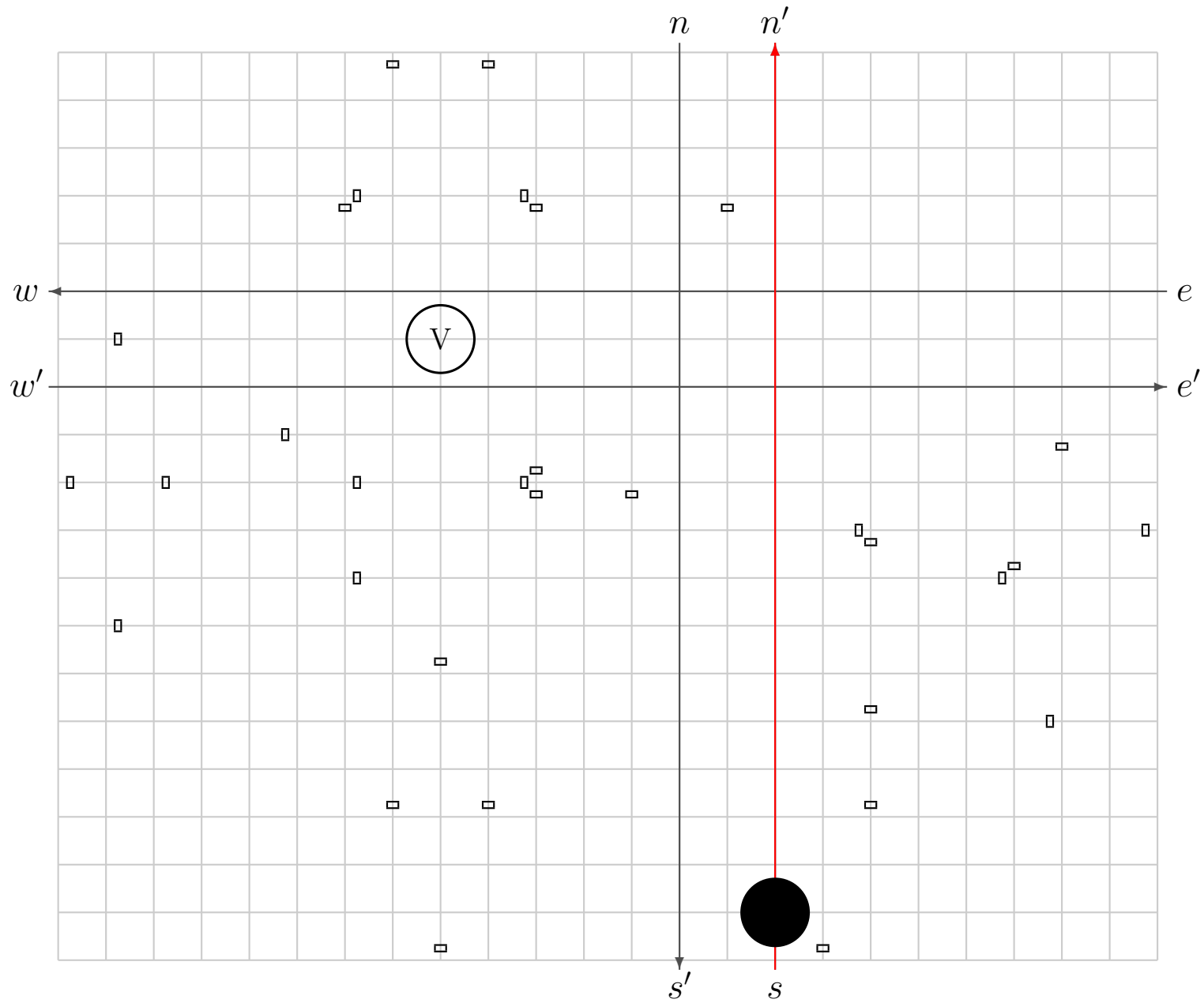
[Morita, 2008]



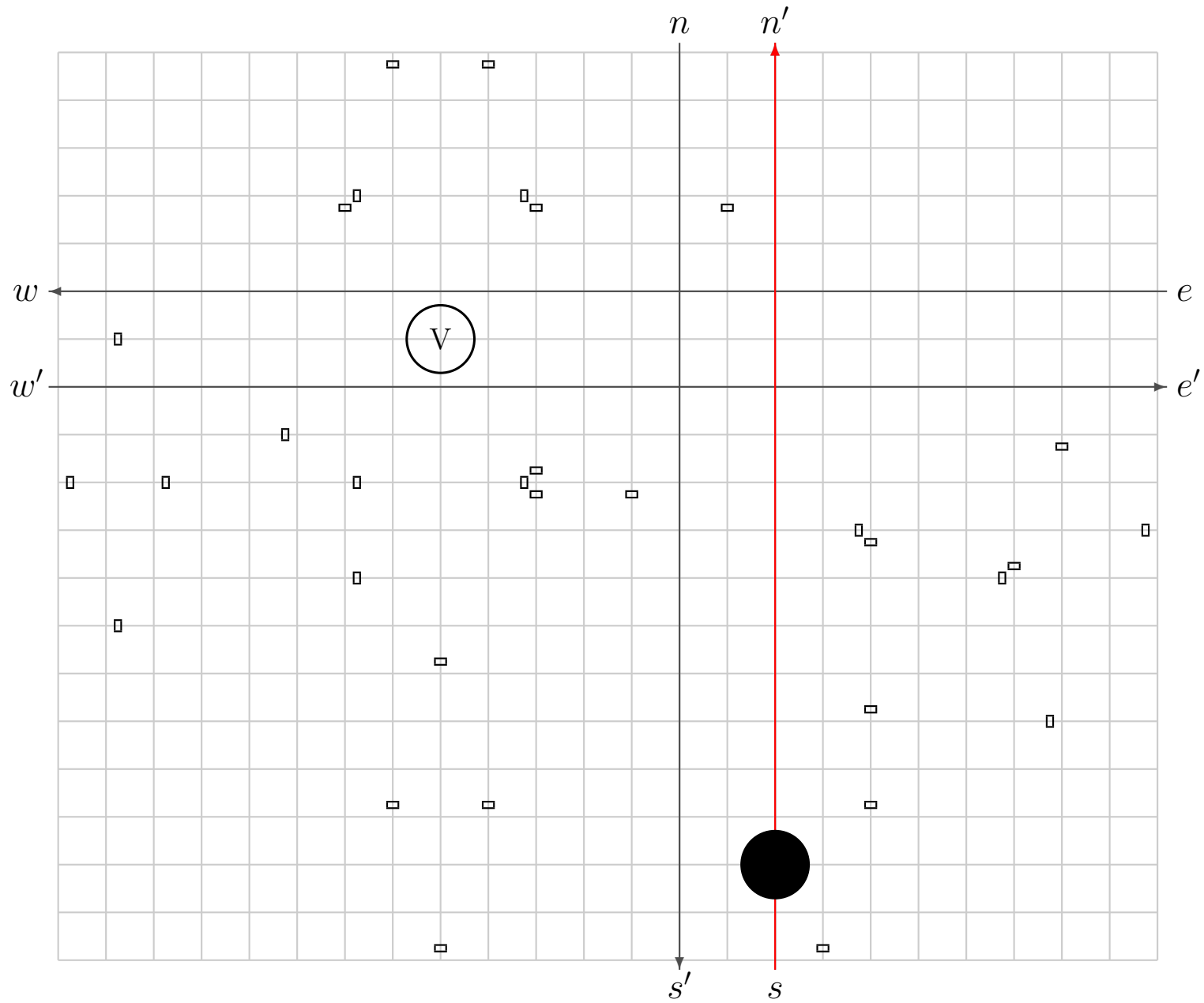
# Parallel Case



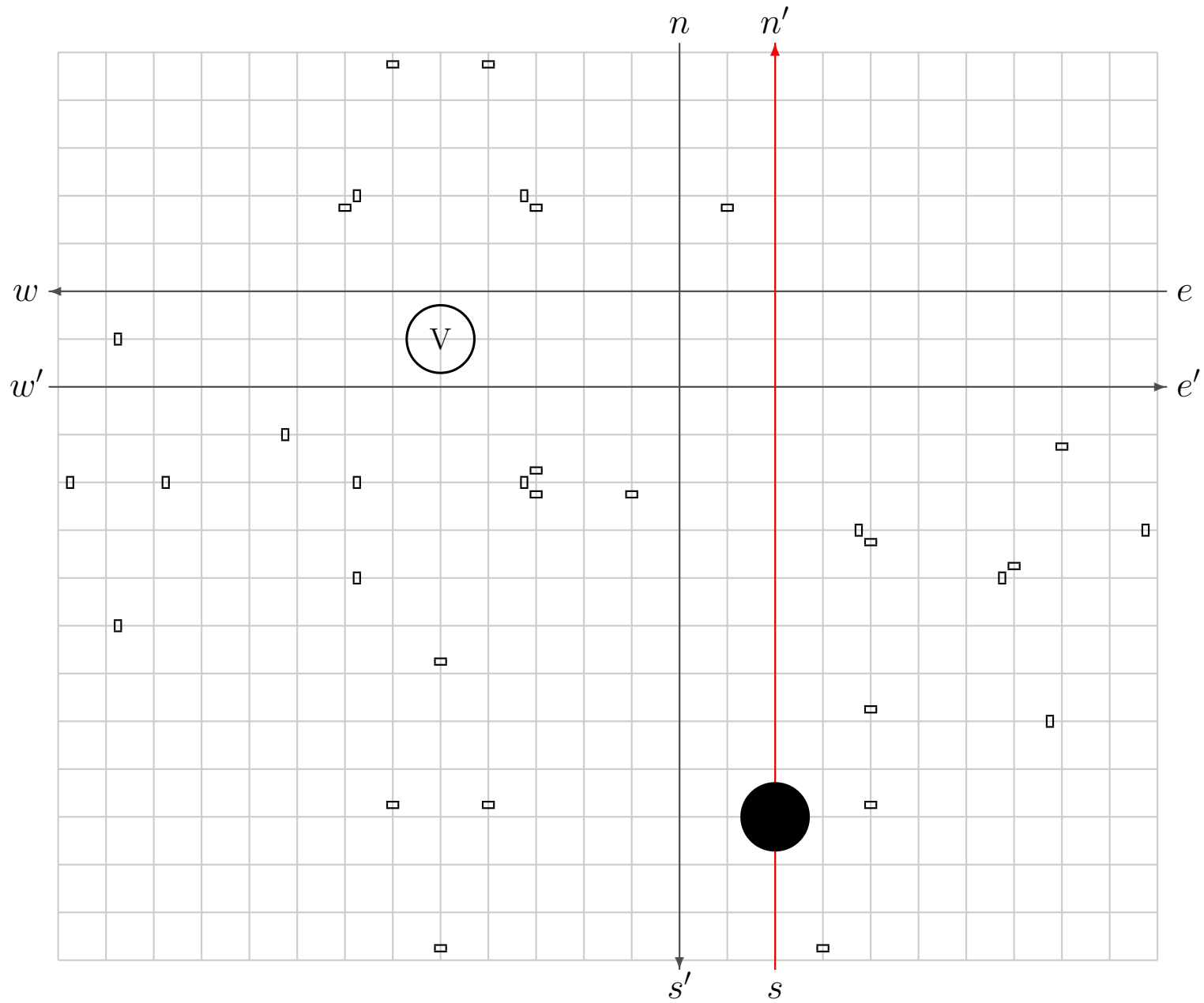
# Movements of Balls (State: $V$ , Input: $s$ )



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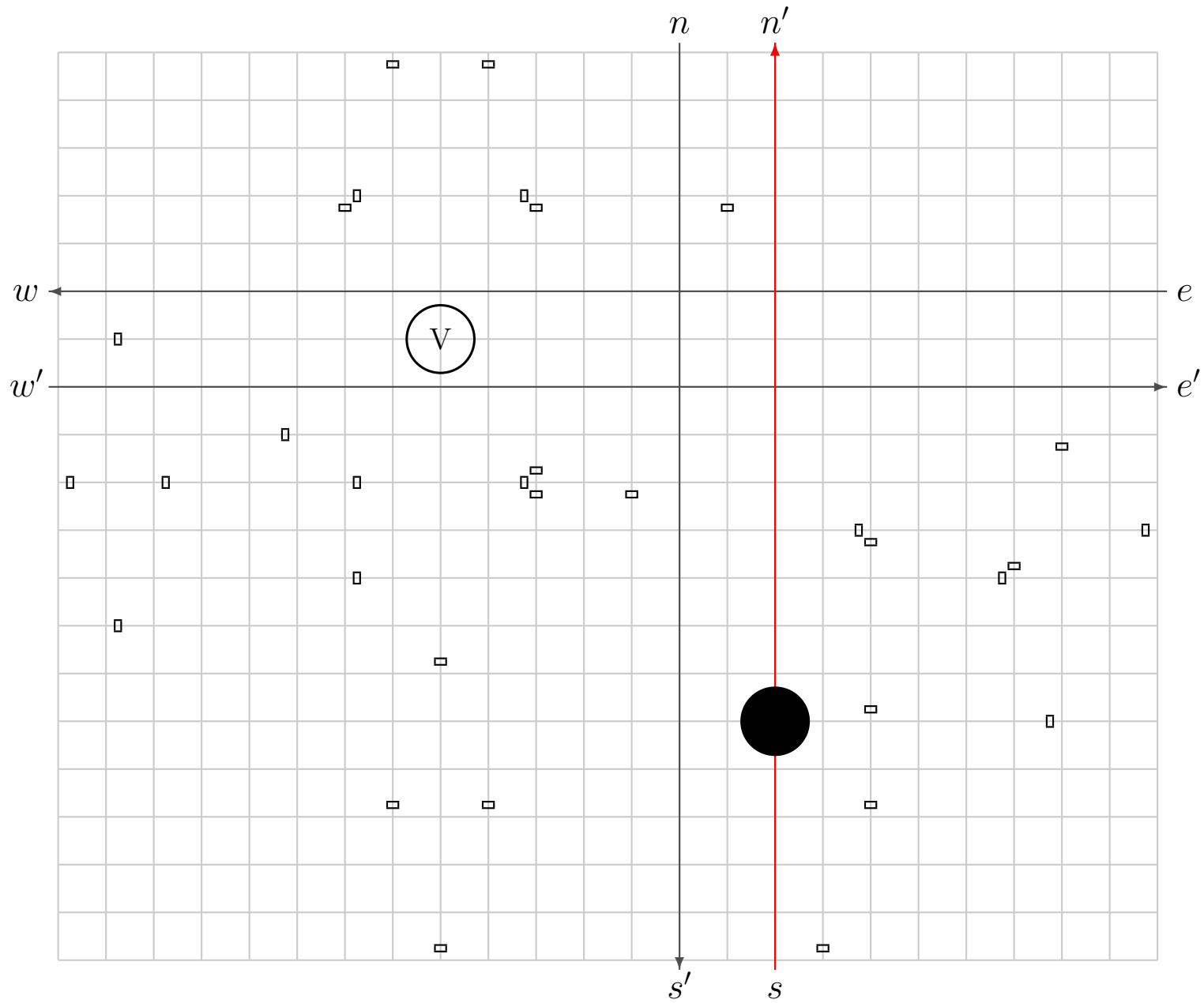


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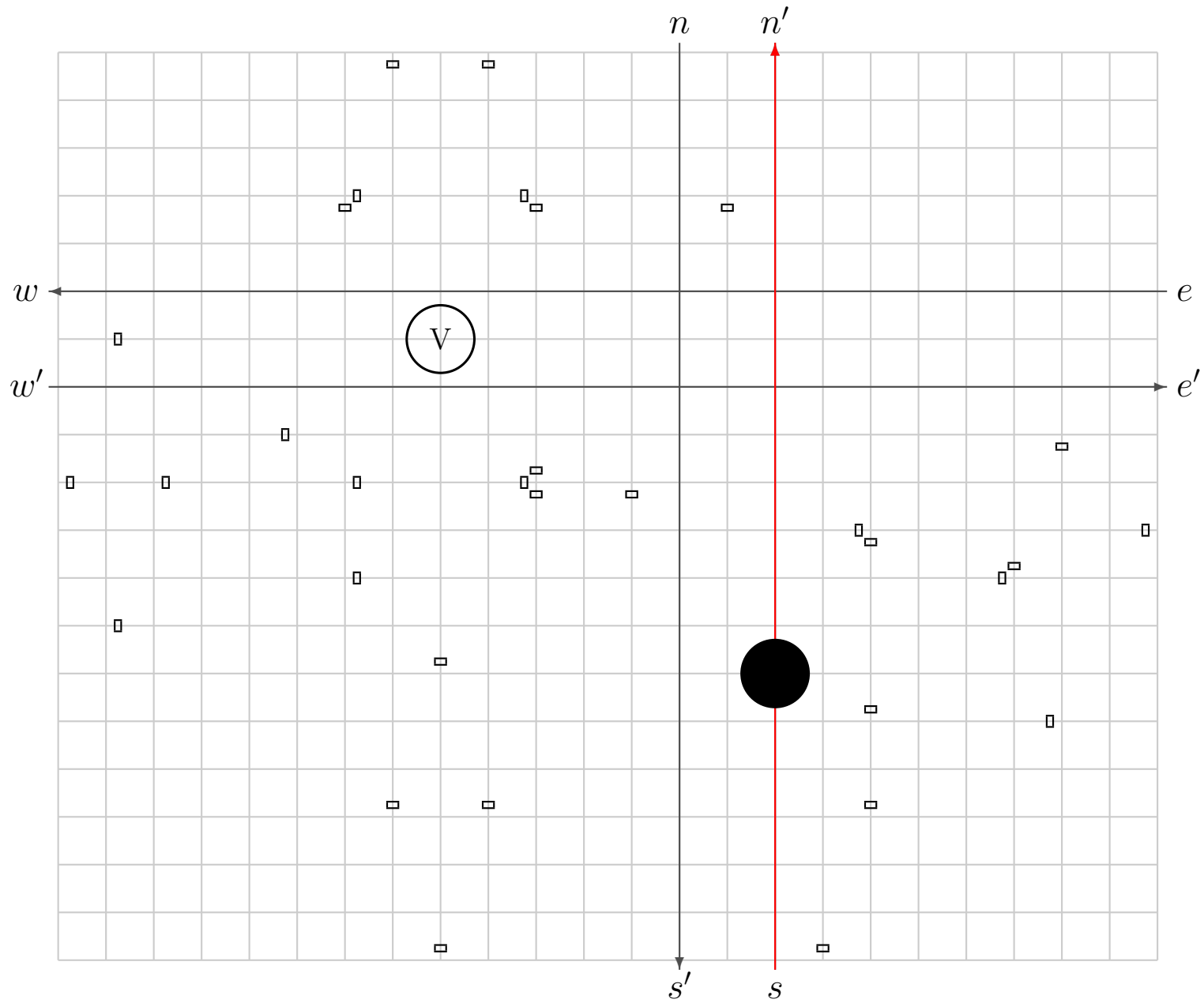




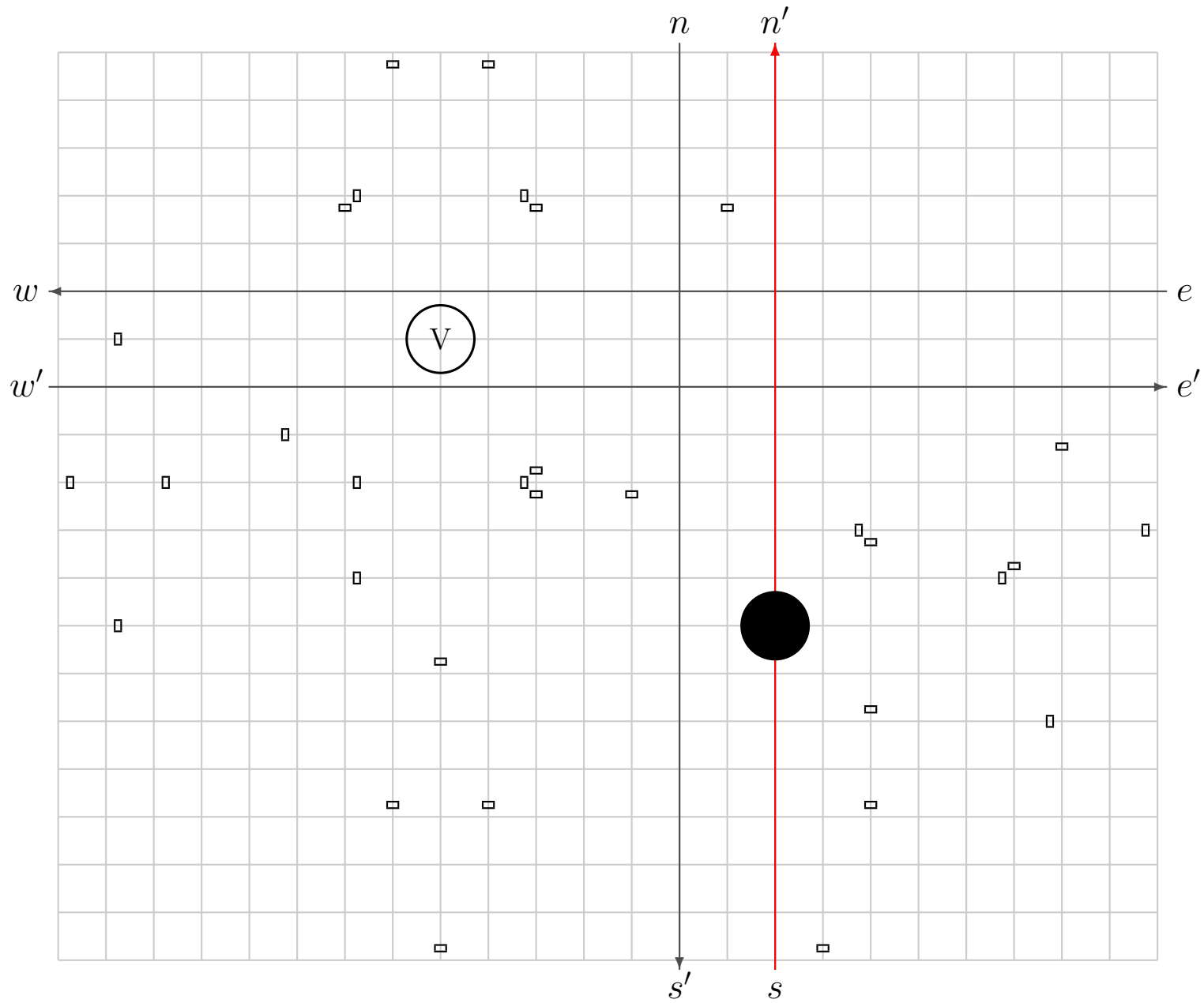
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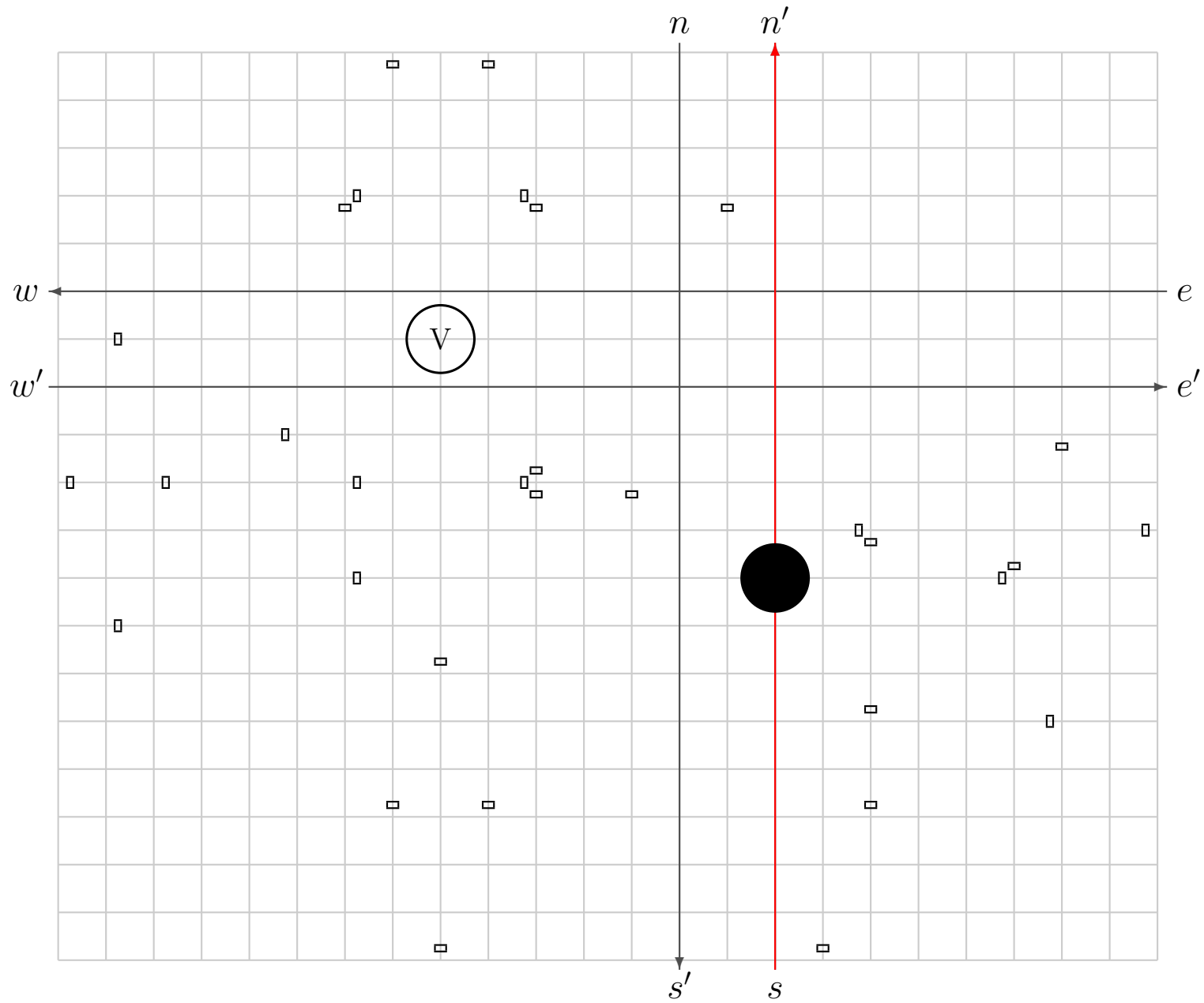


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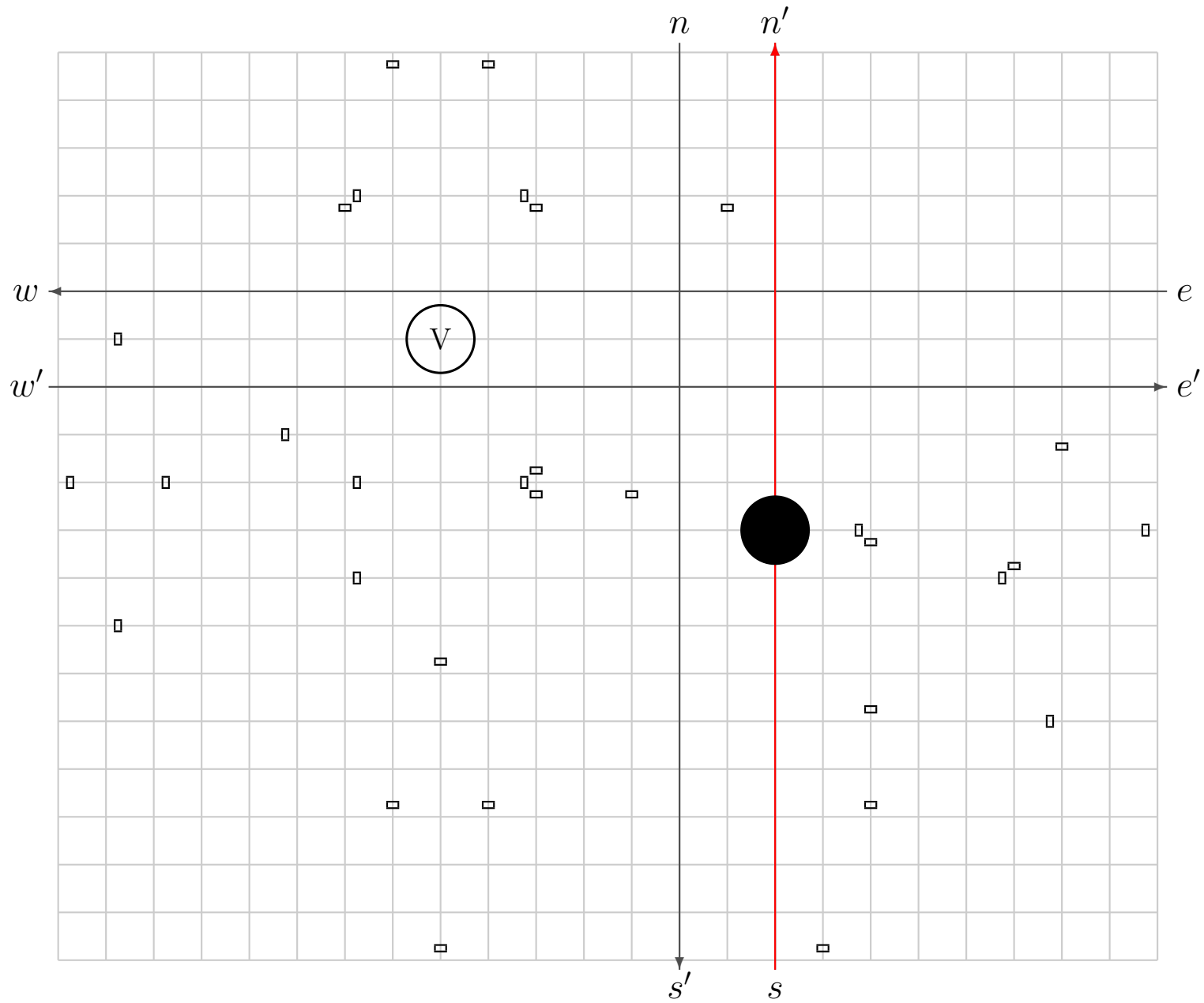




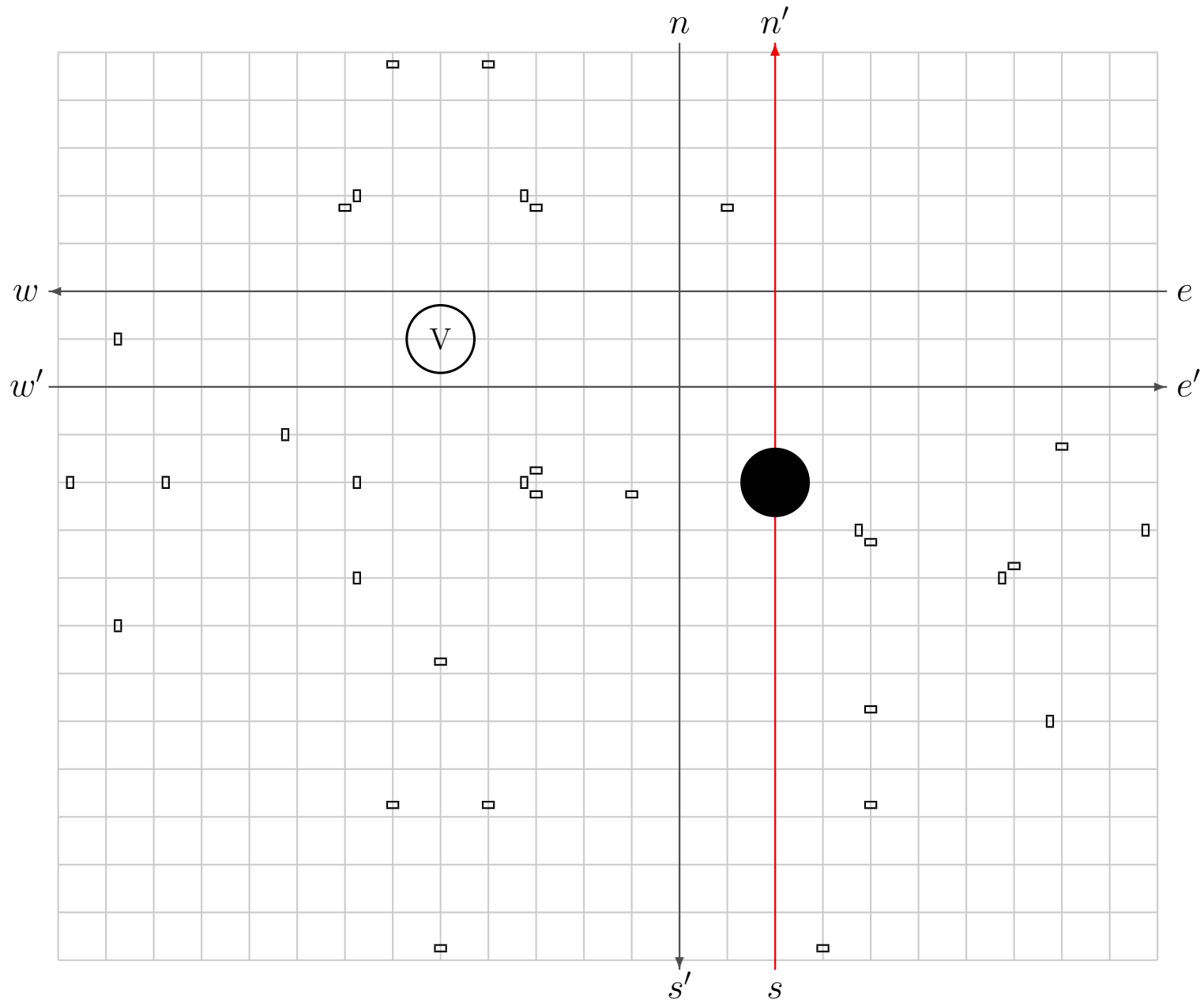
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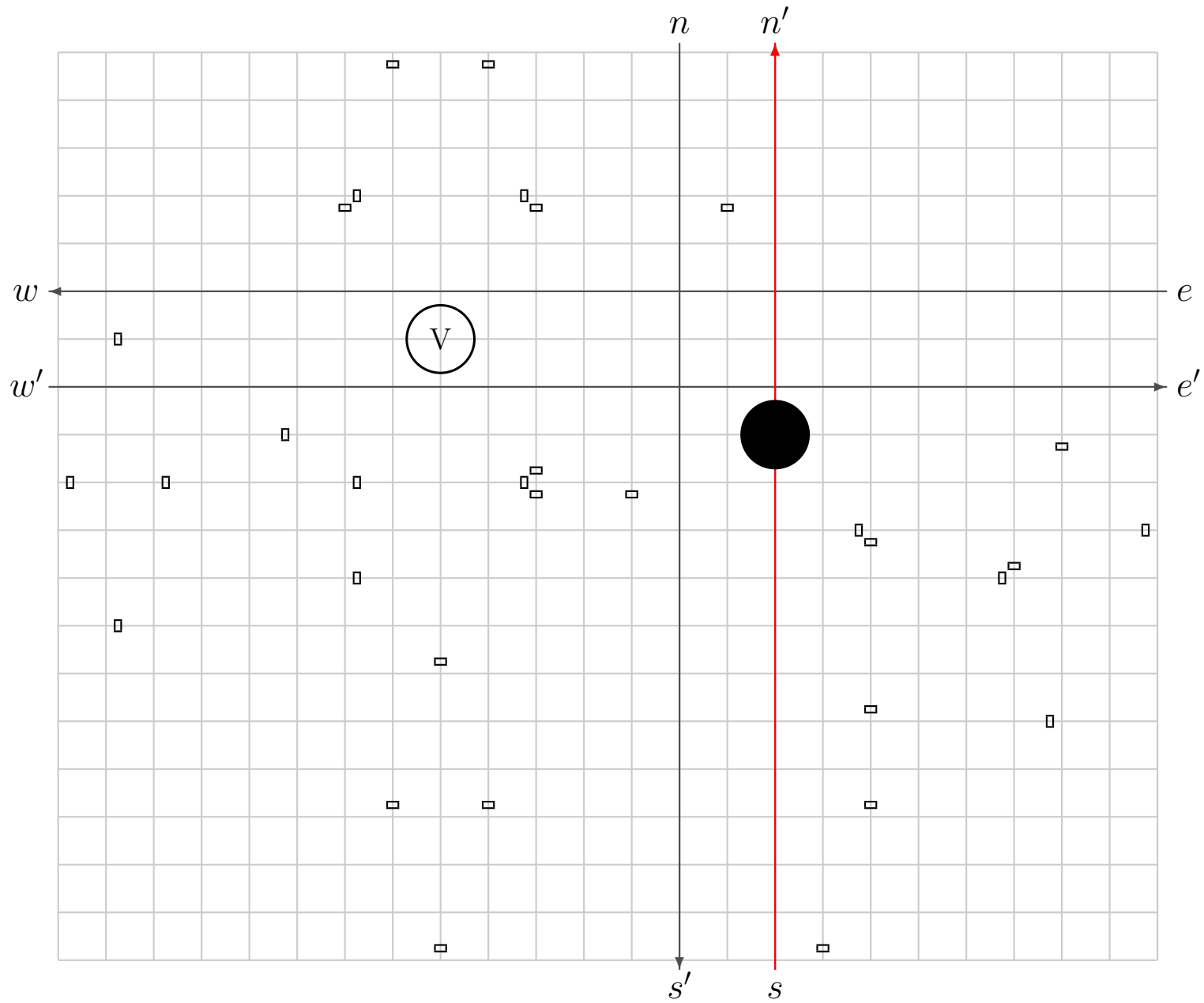
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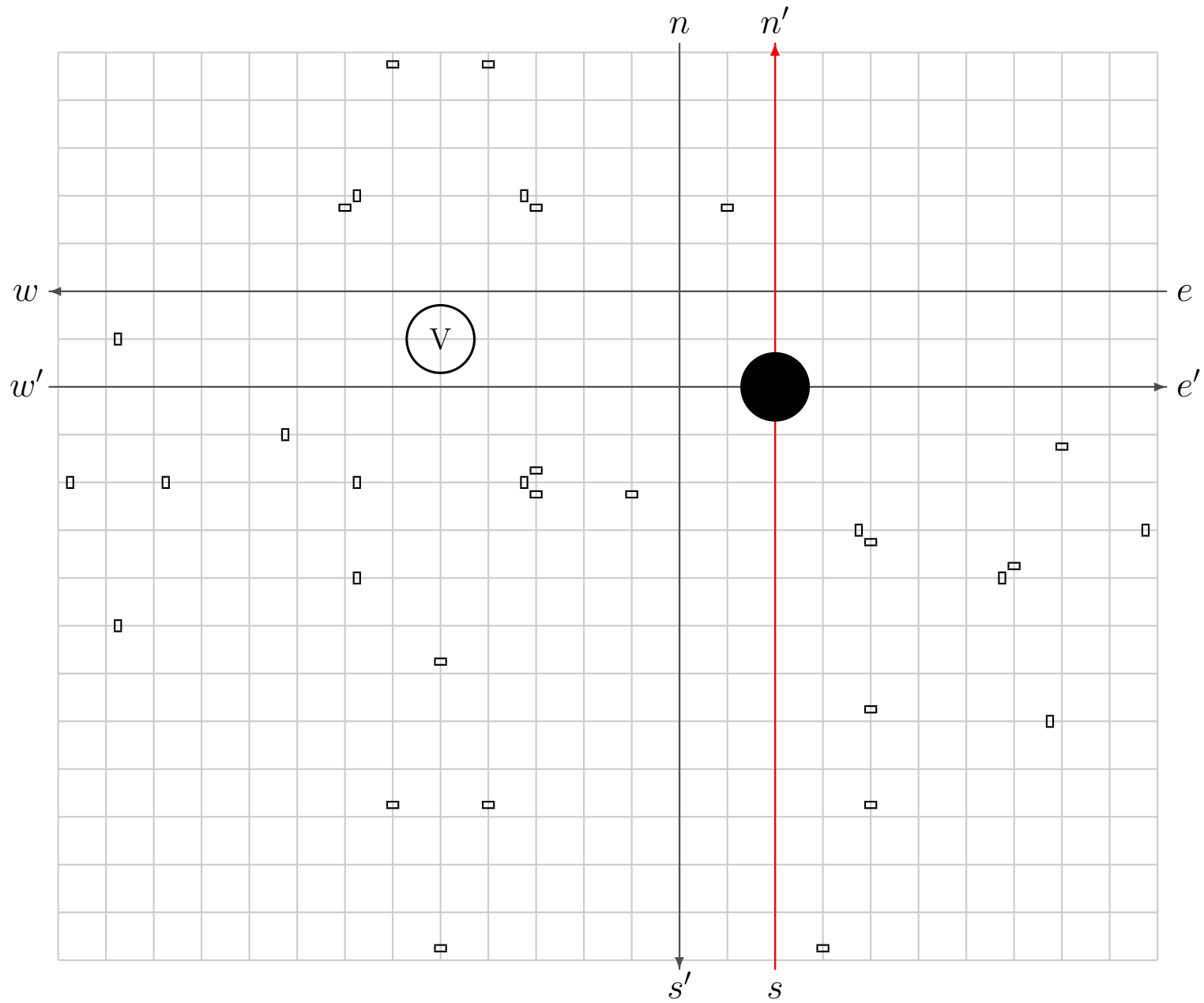
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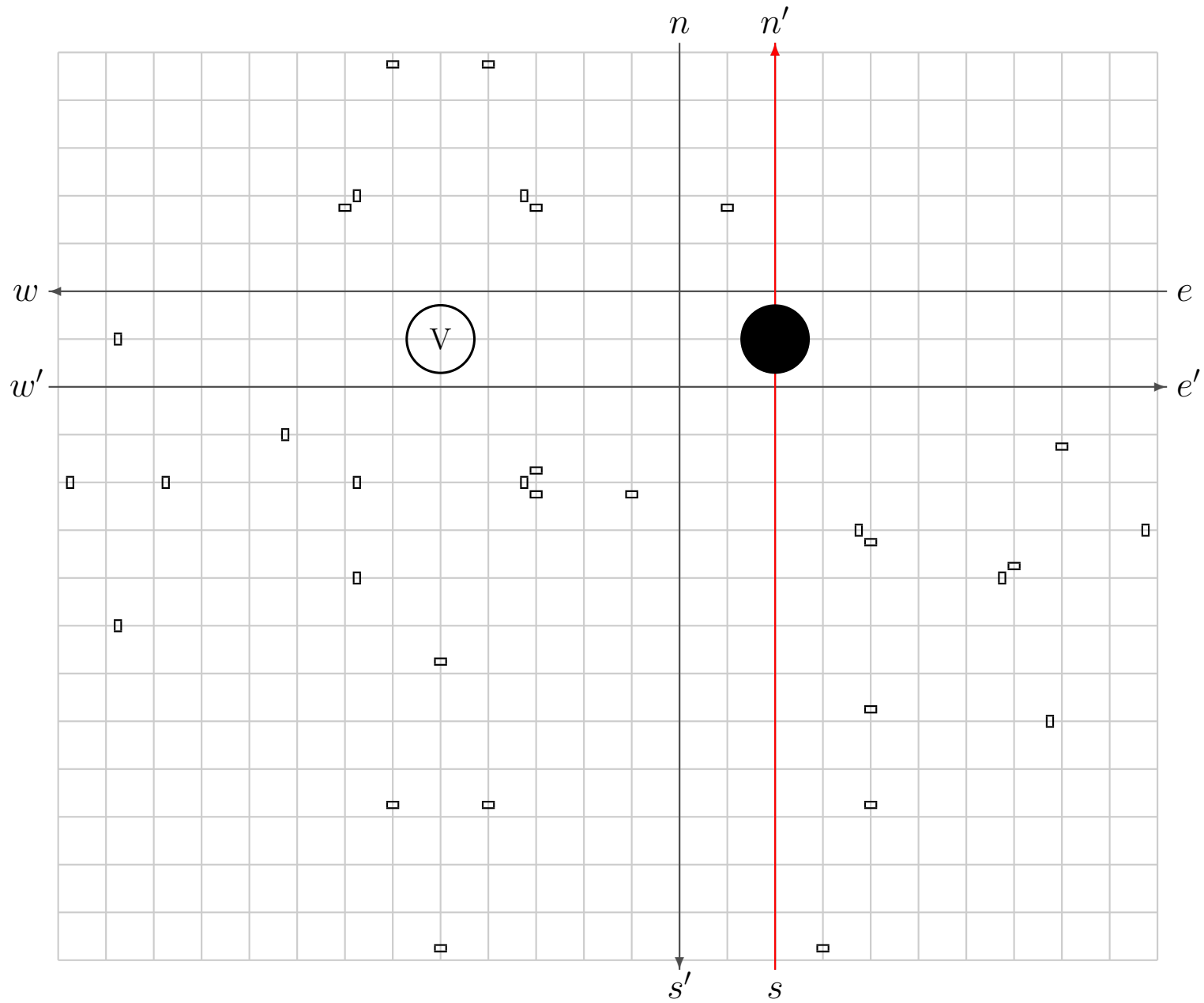
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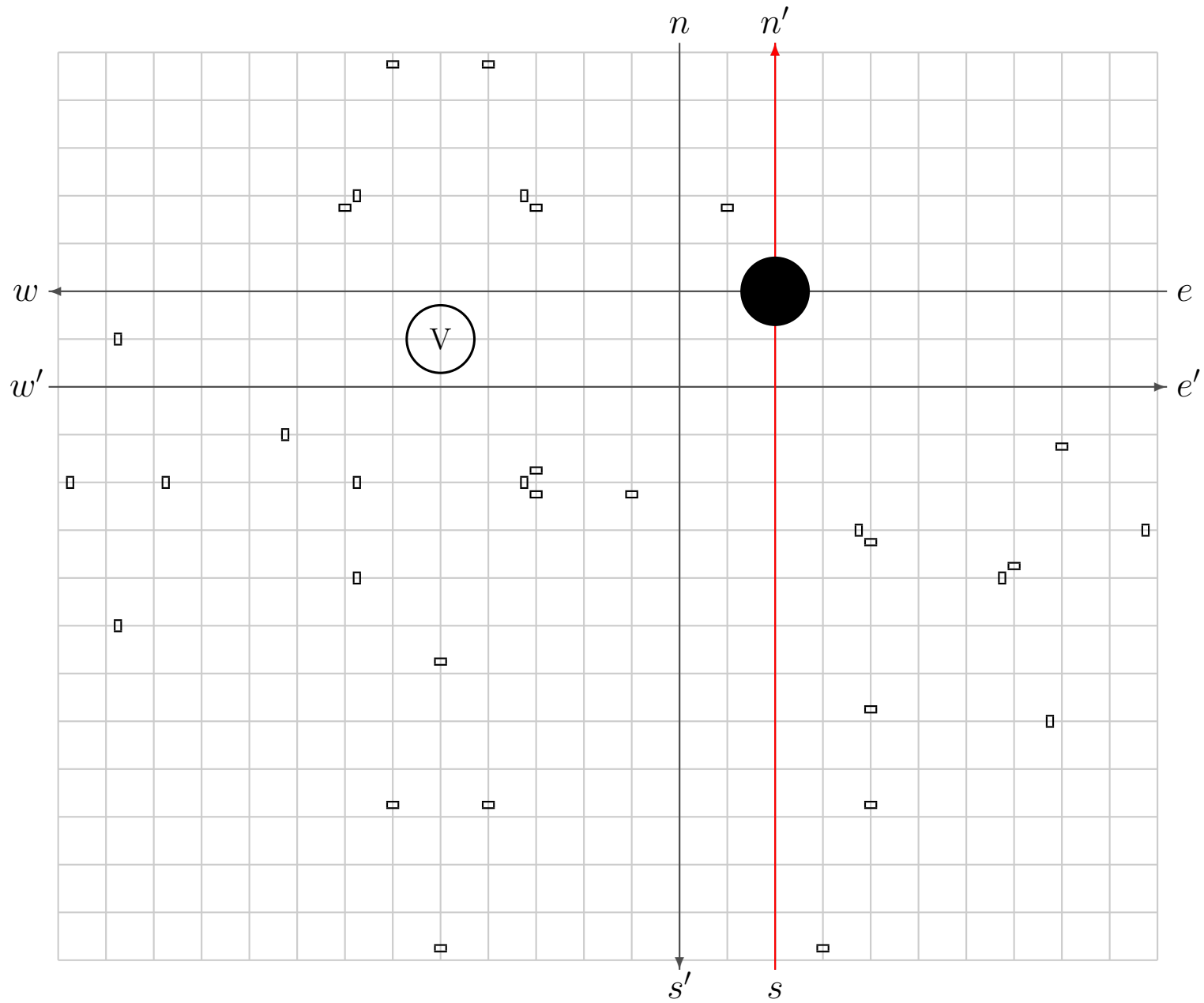
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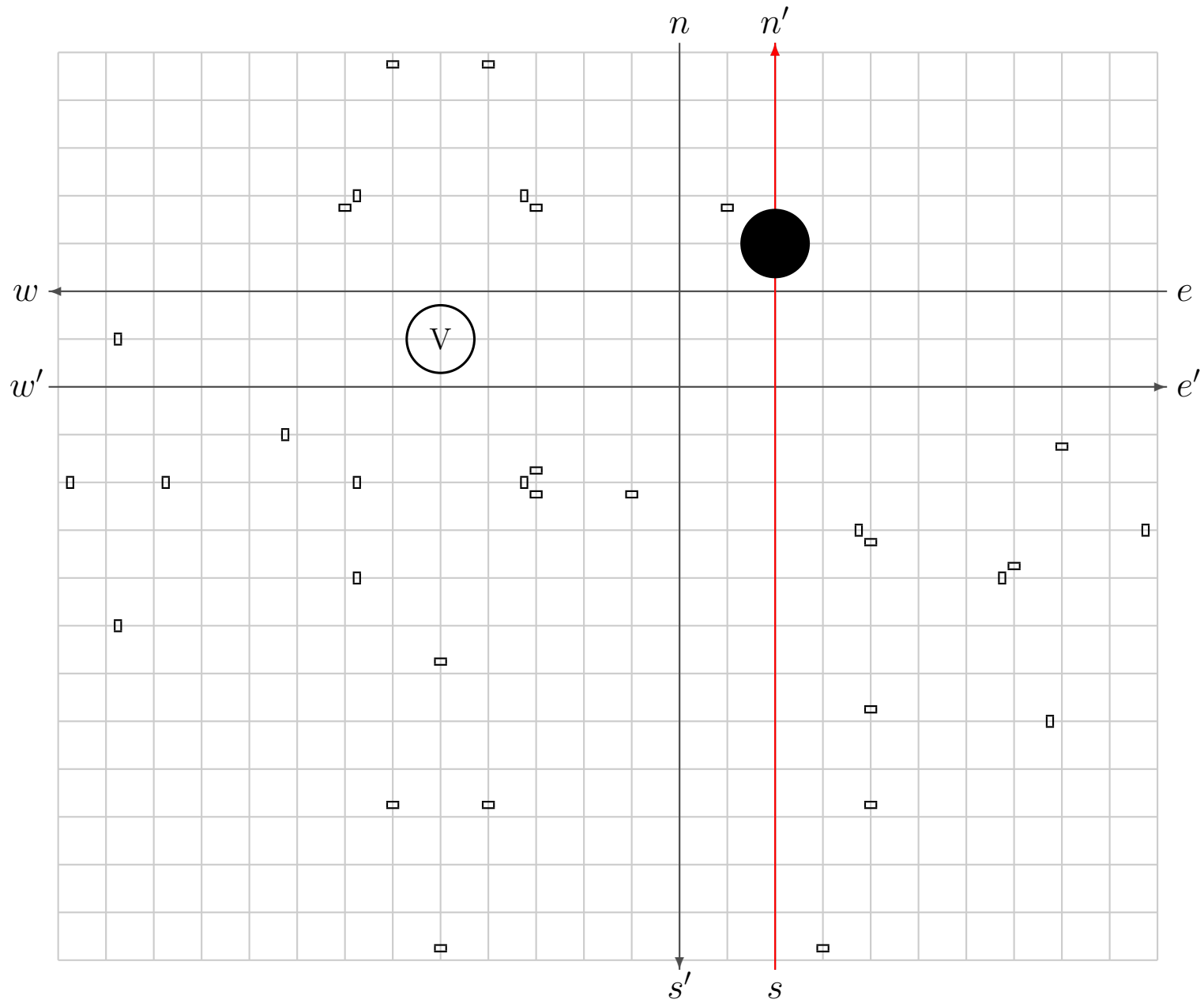
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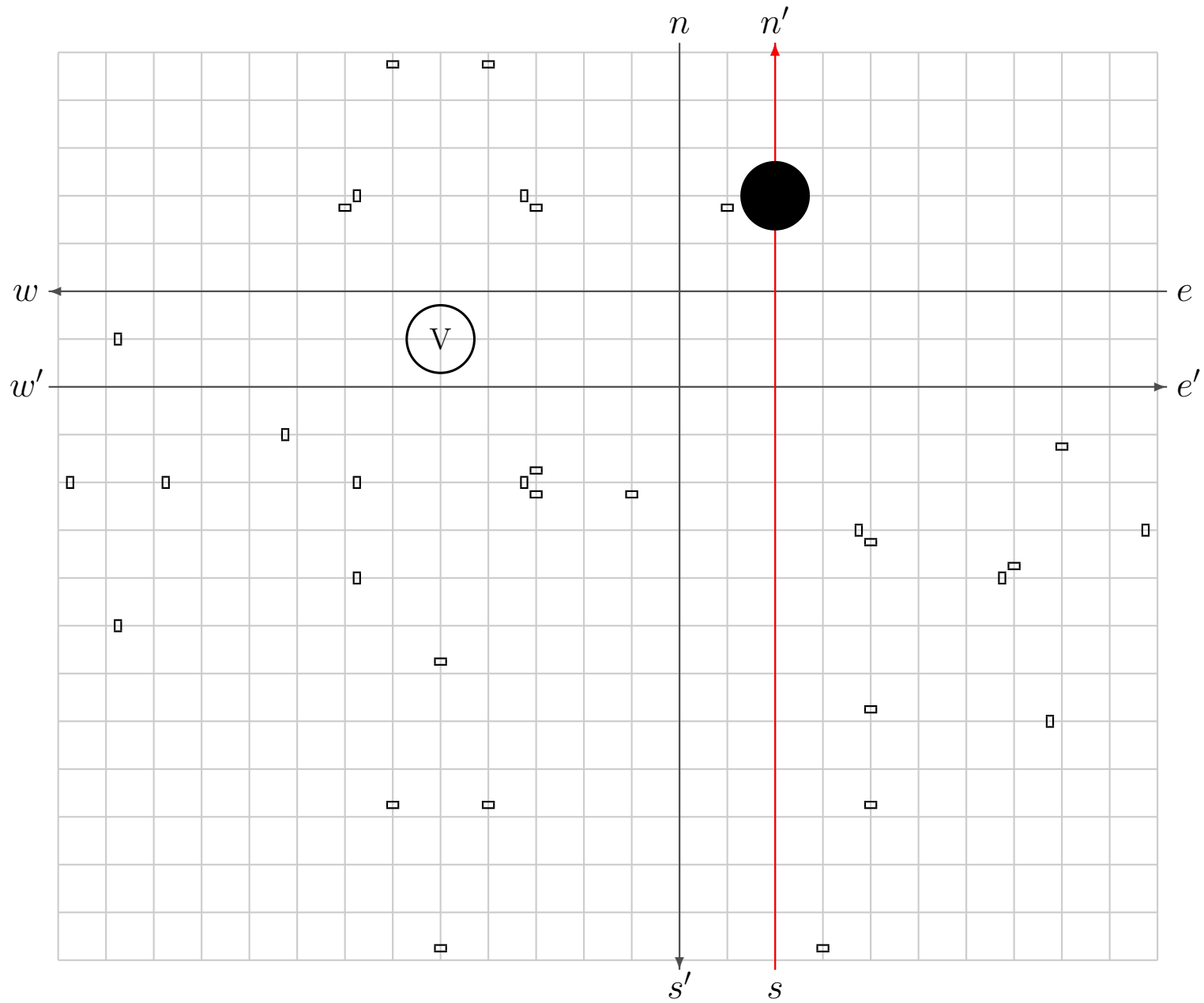


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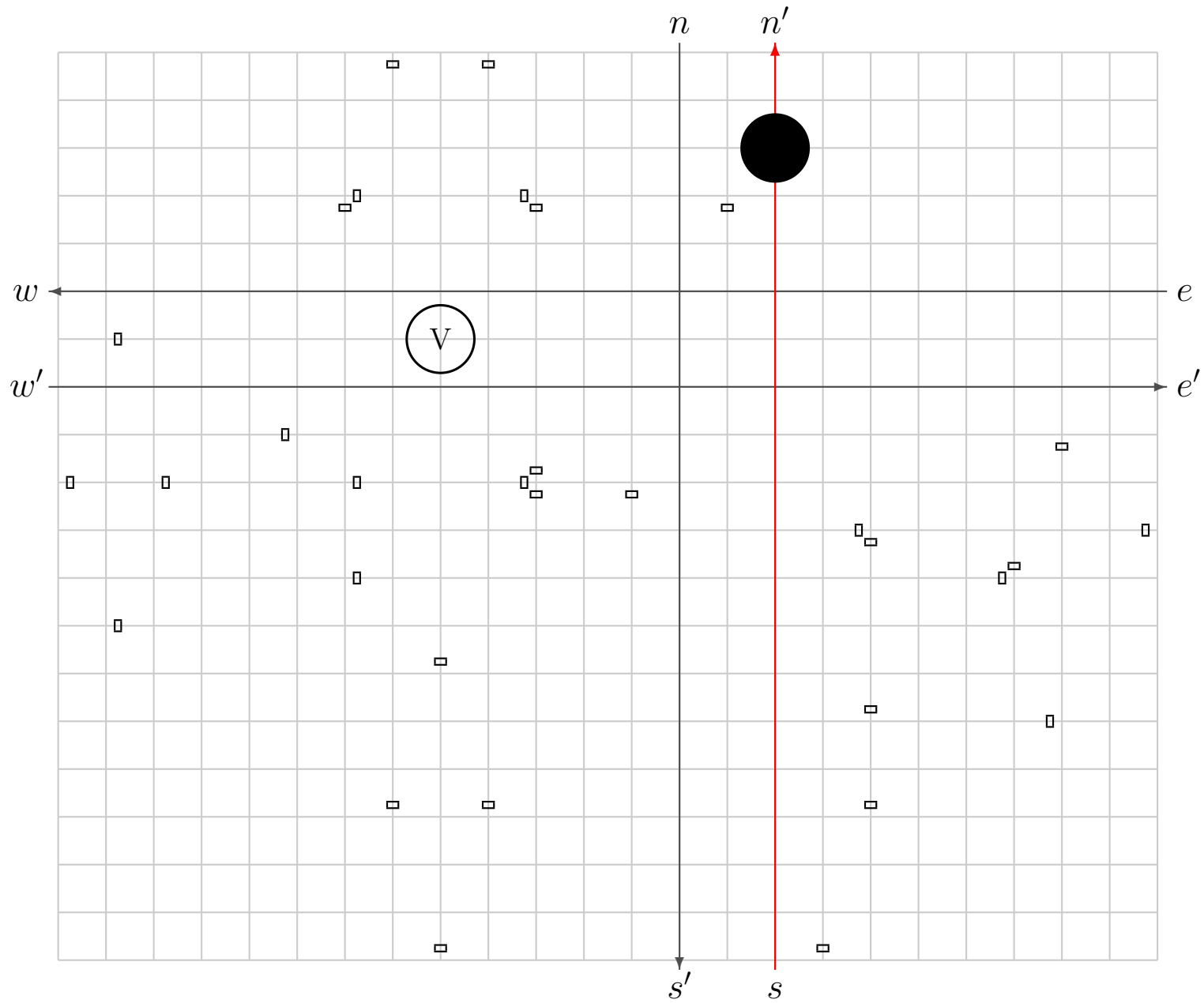




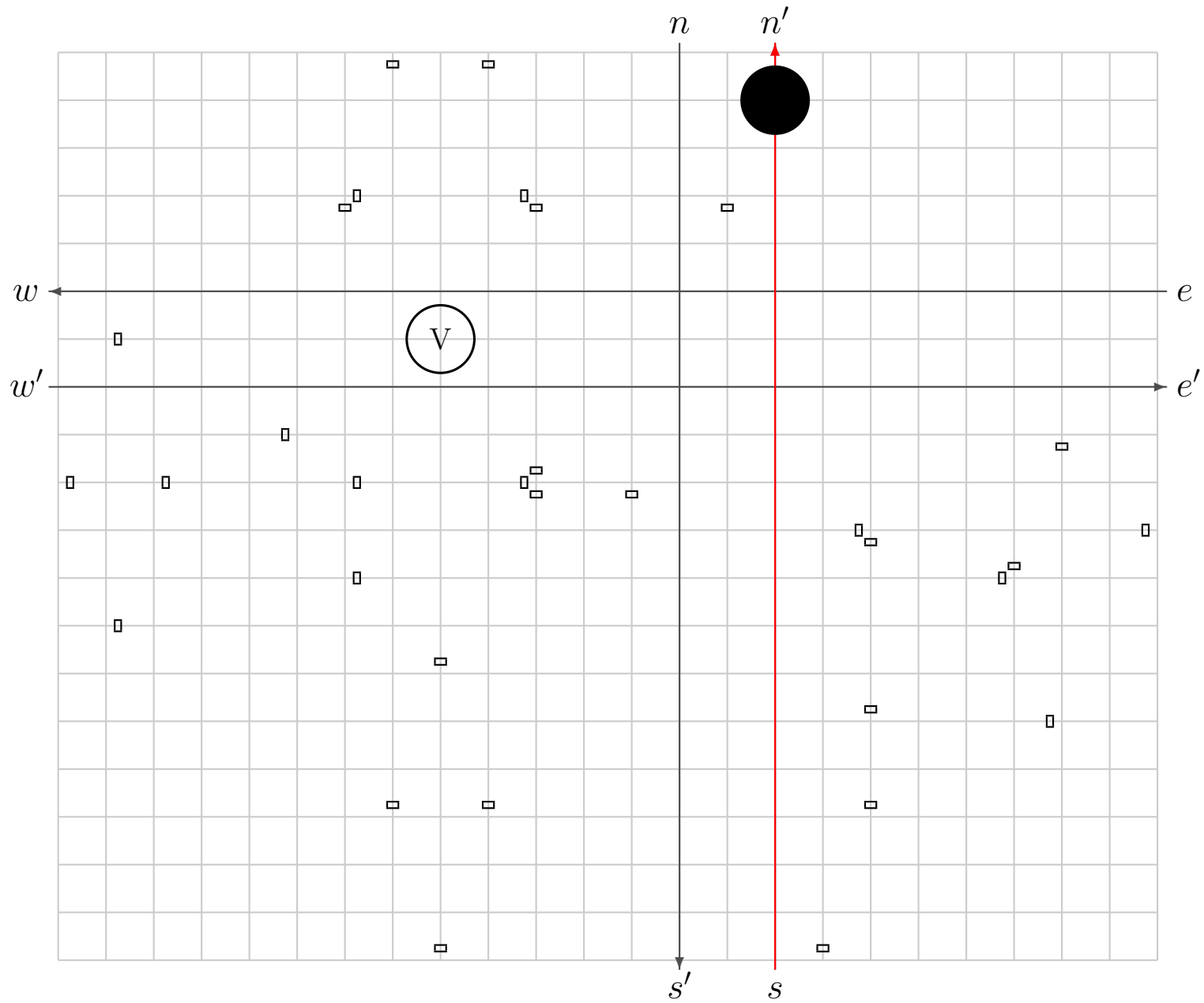
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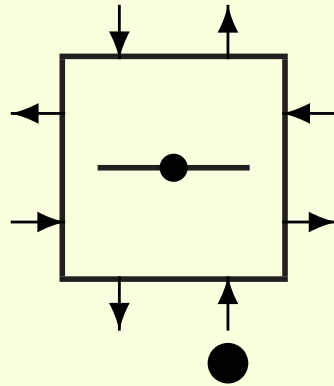


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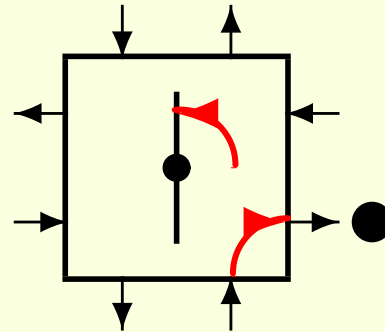


# Orthogonal Case

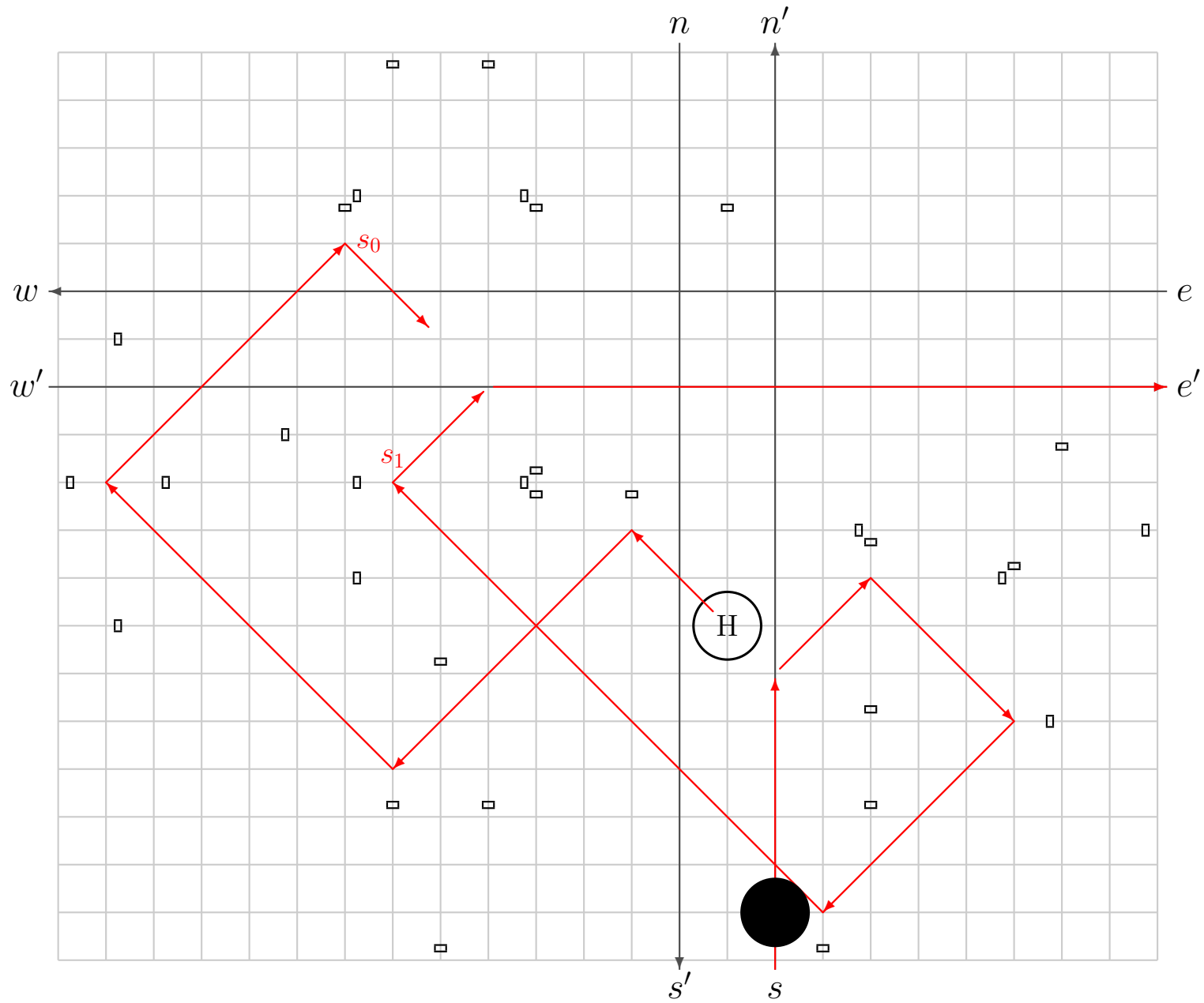
$t$



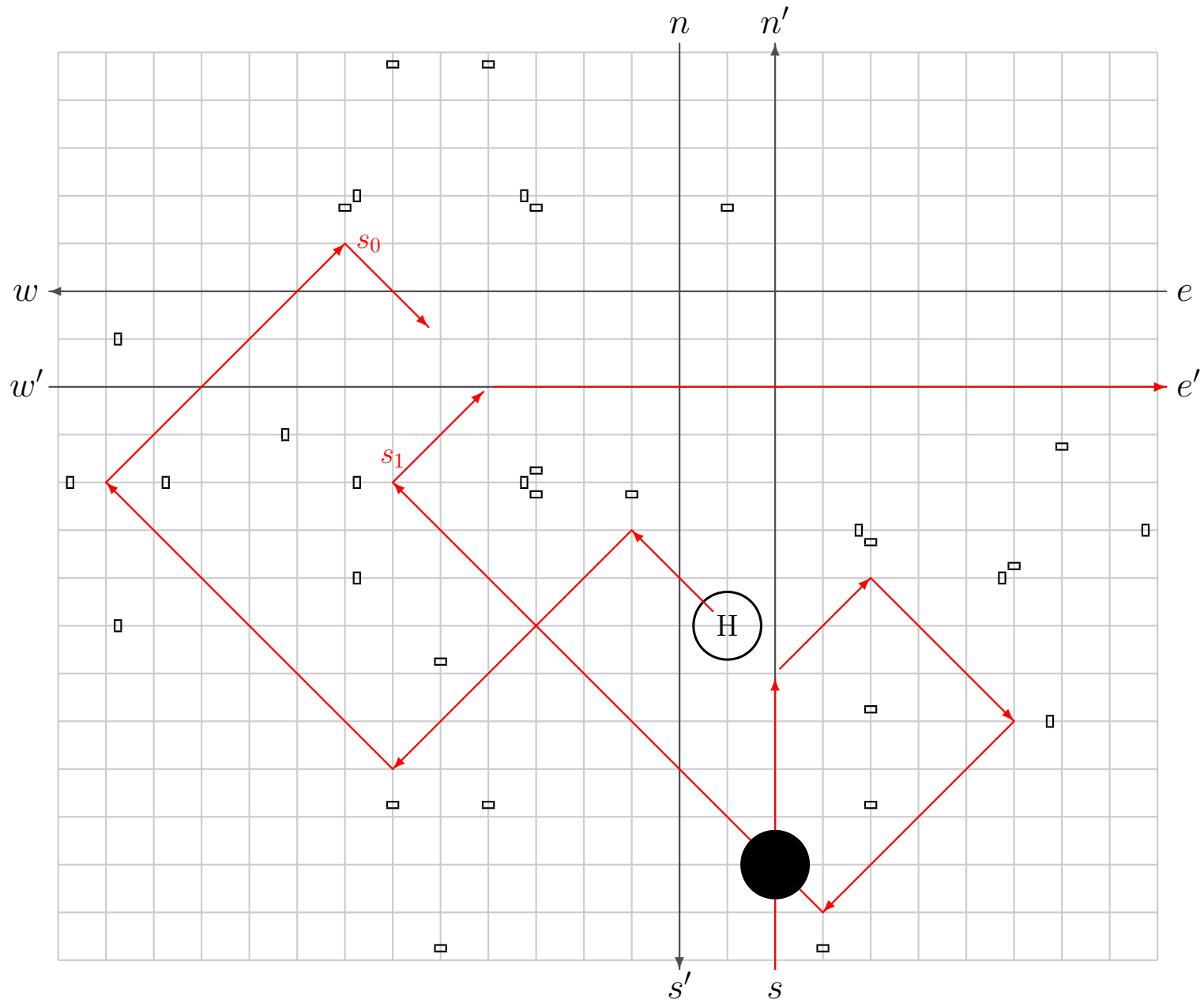
$t + 1$



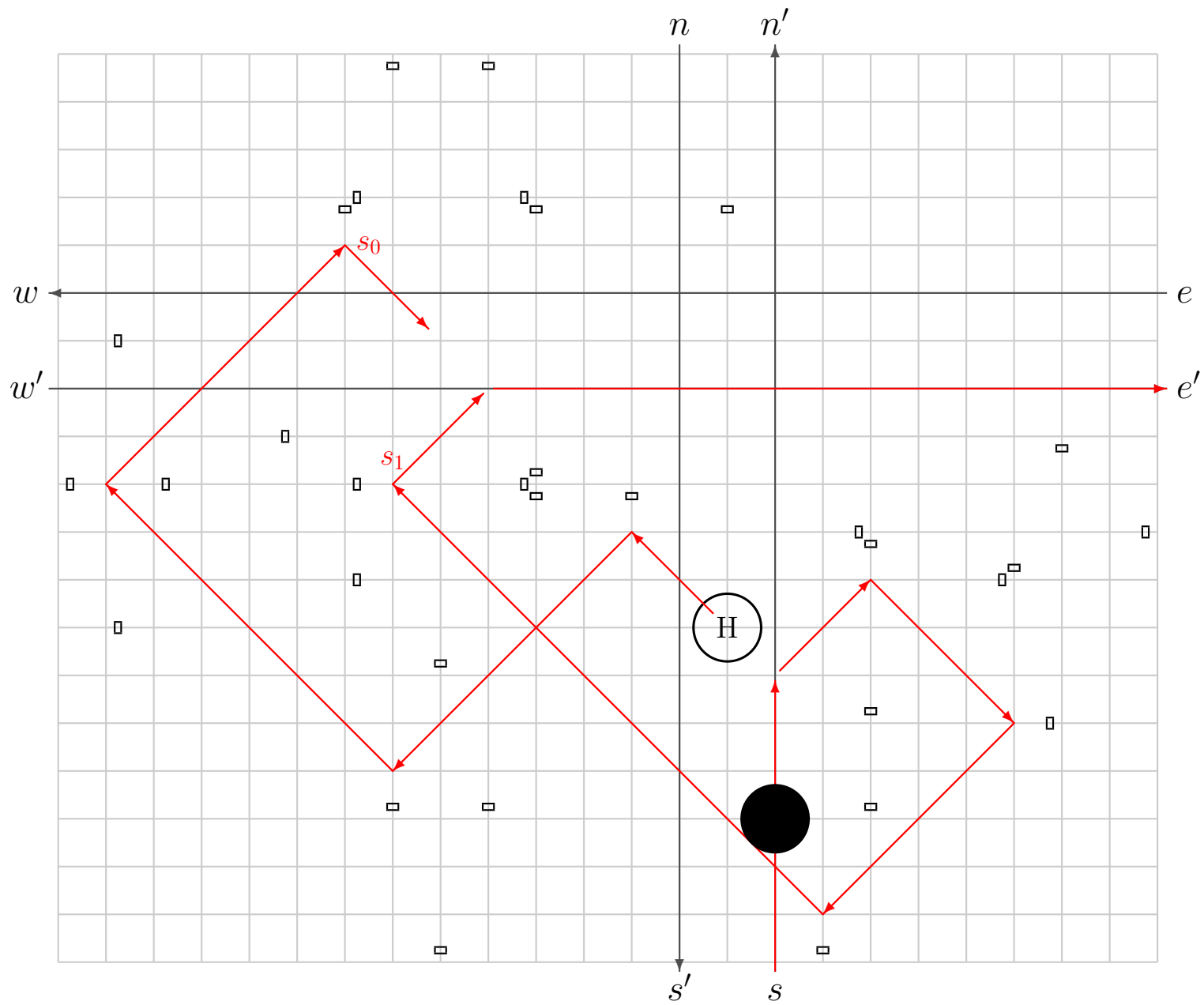
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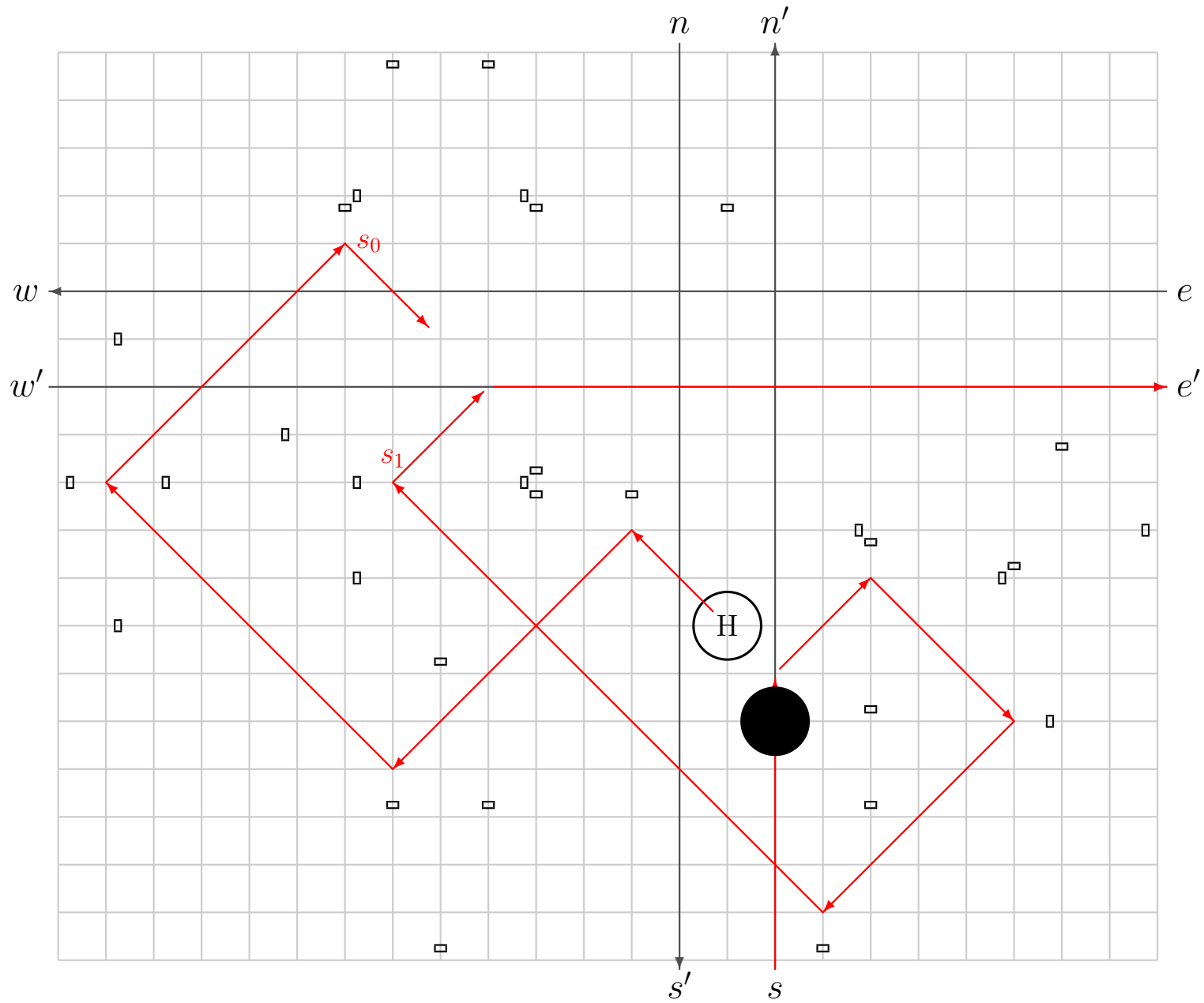
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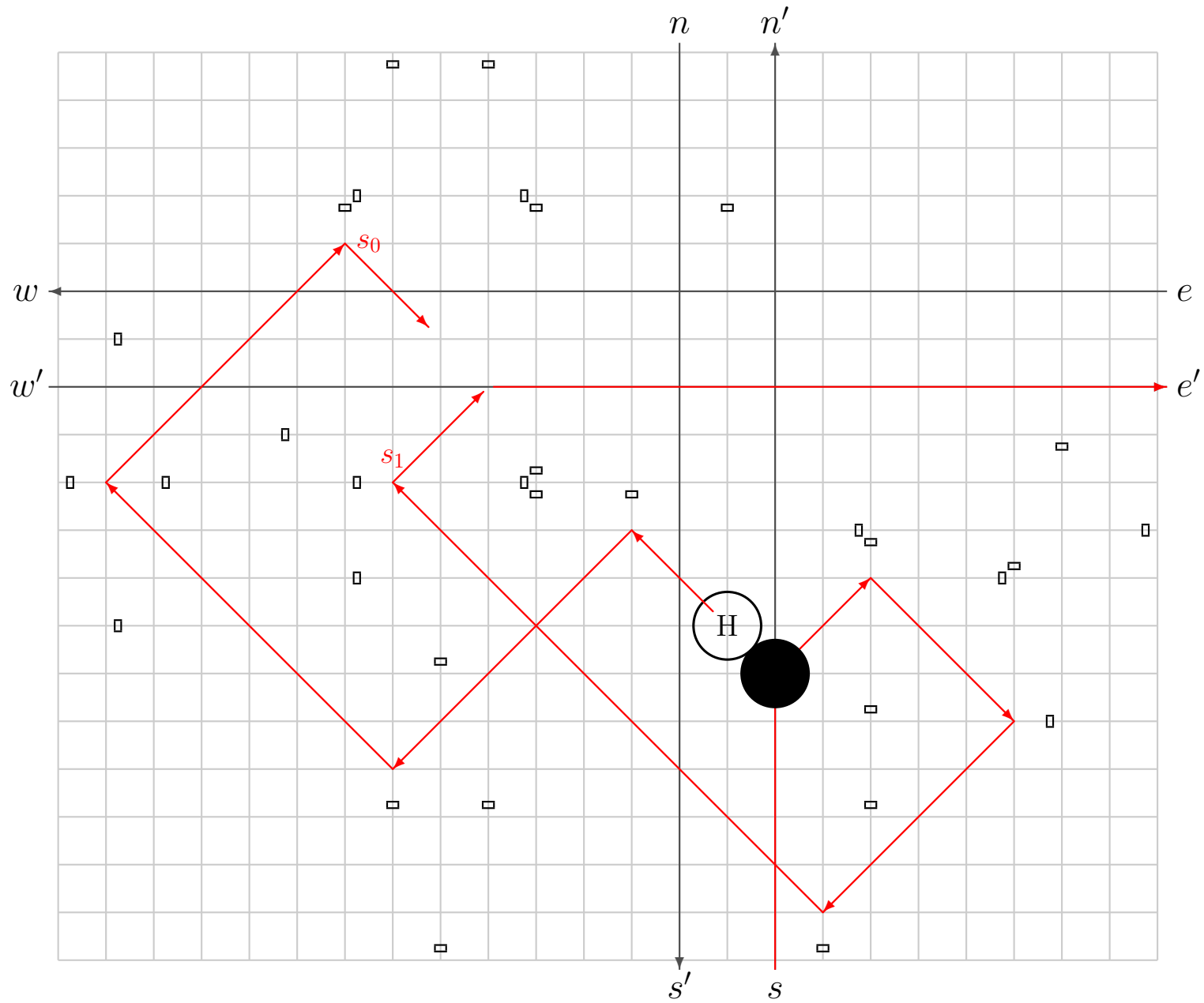




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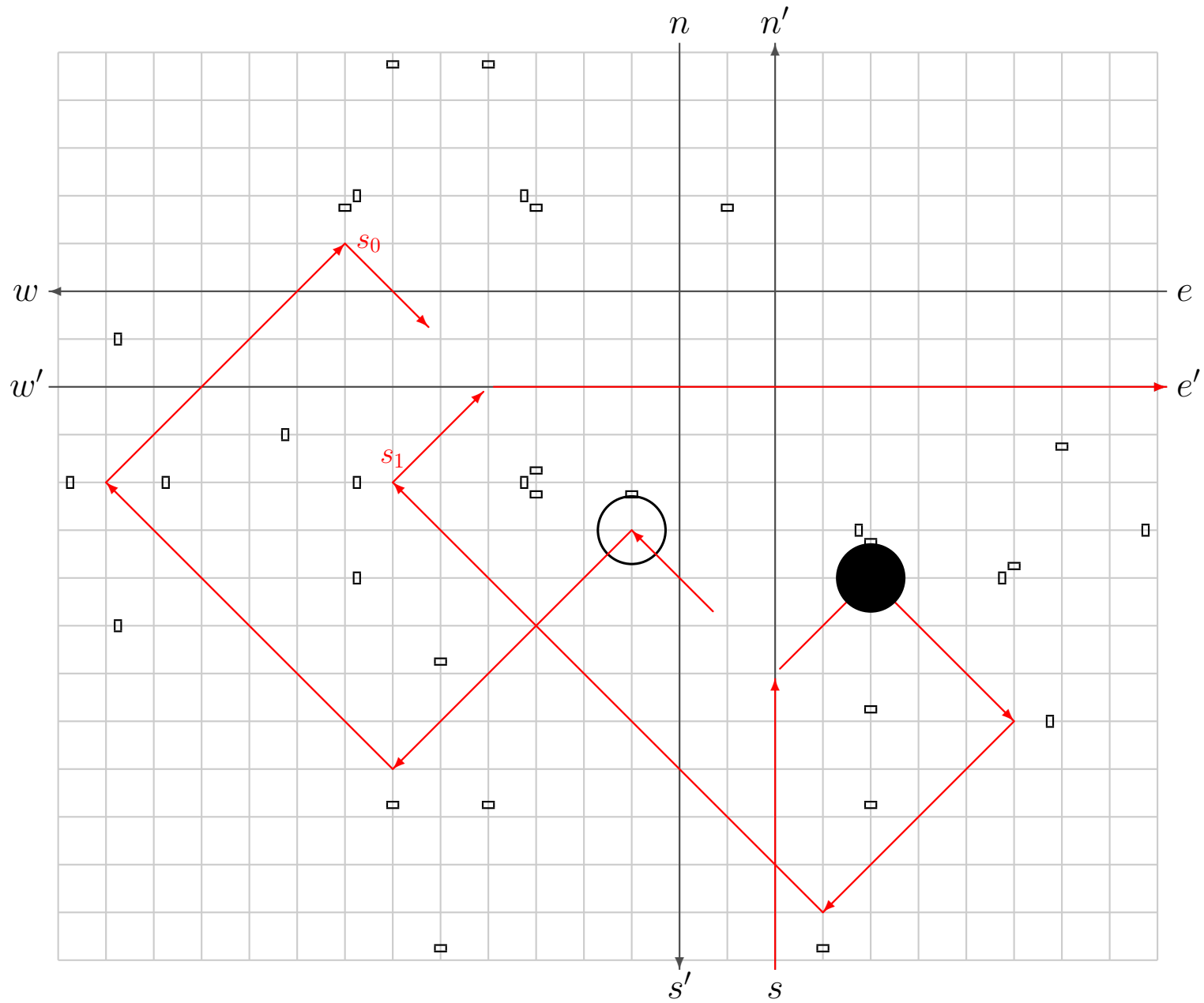


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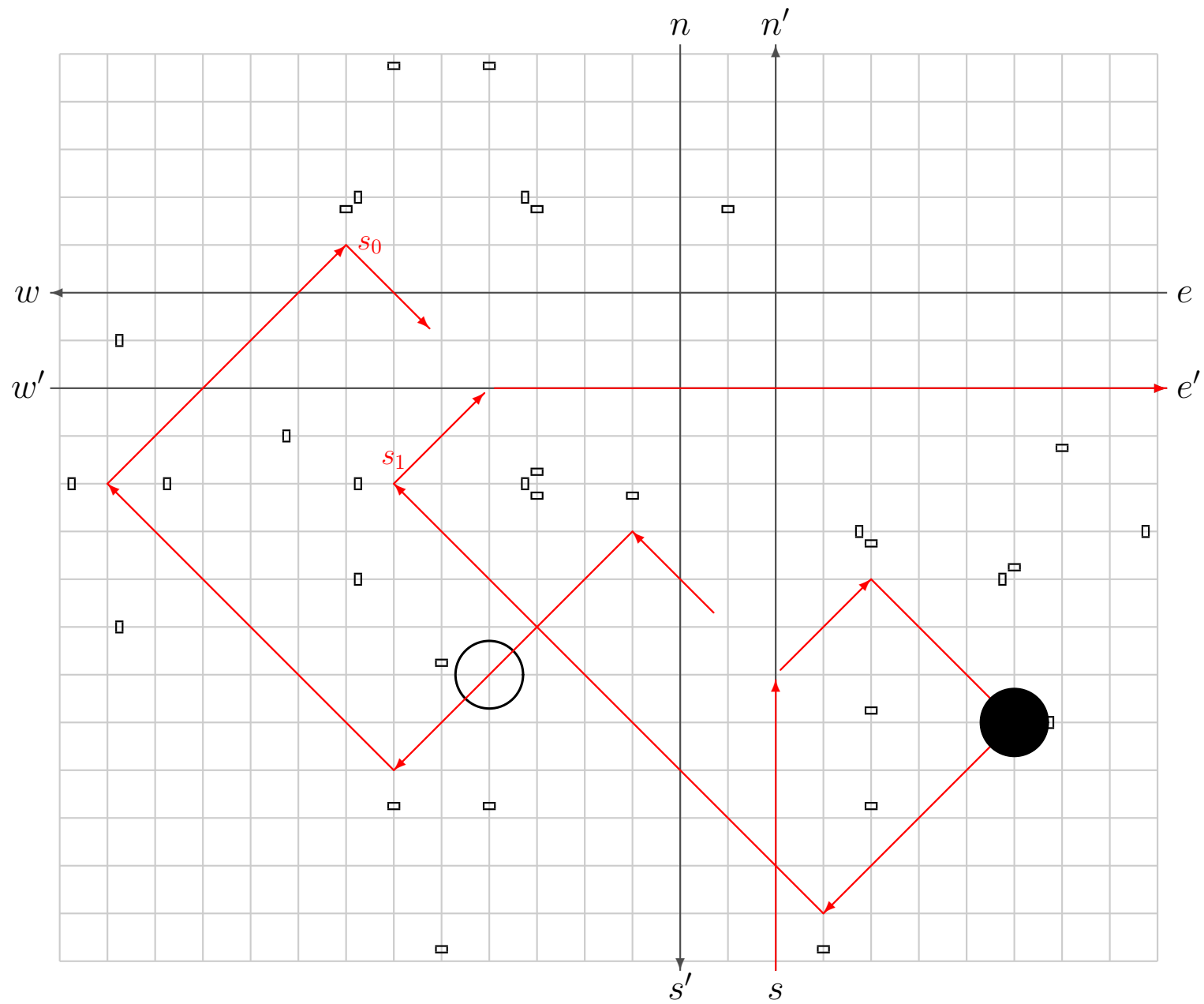
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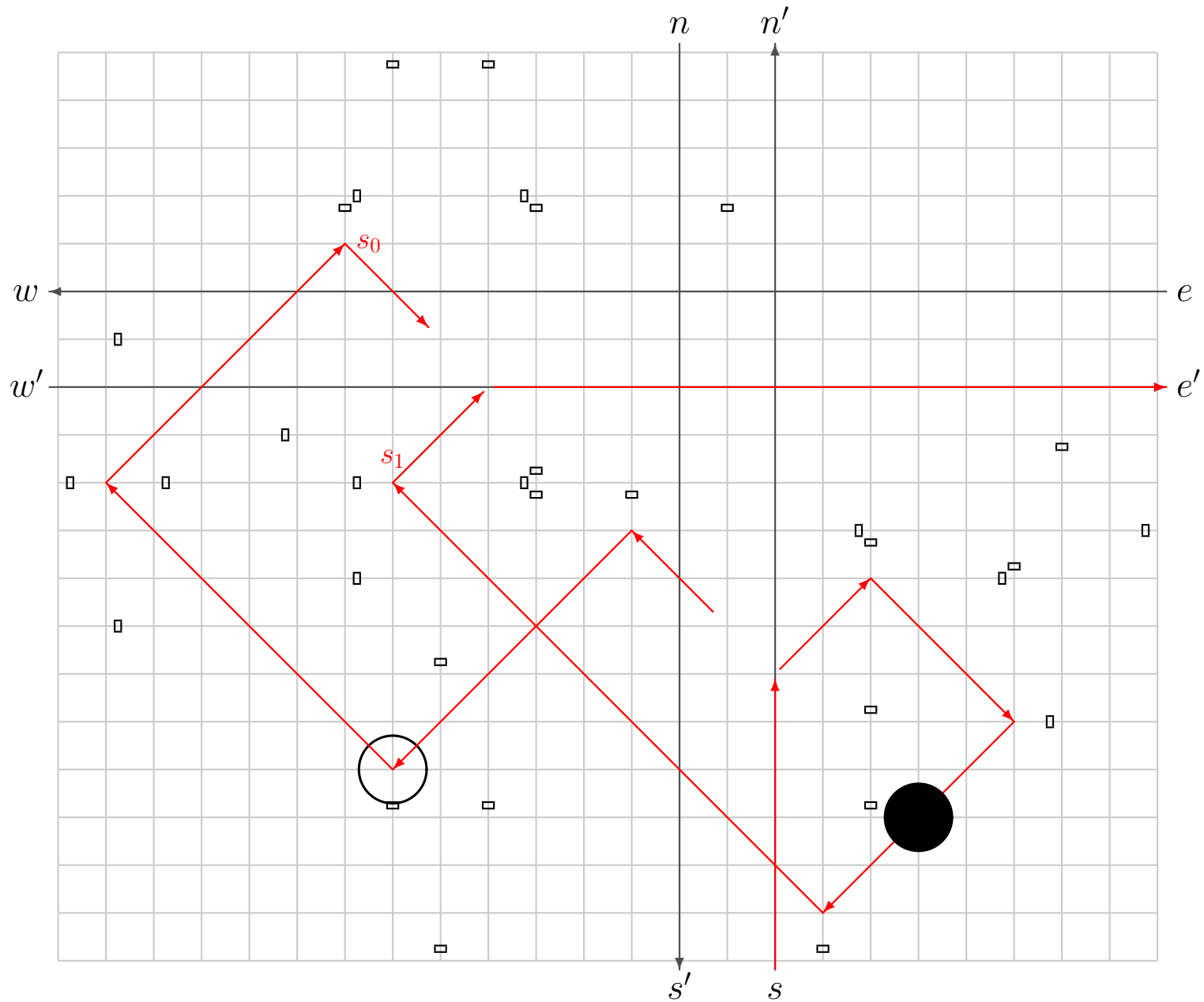
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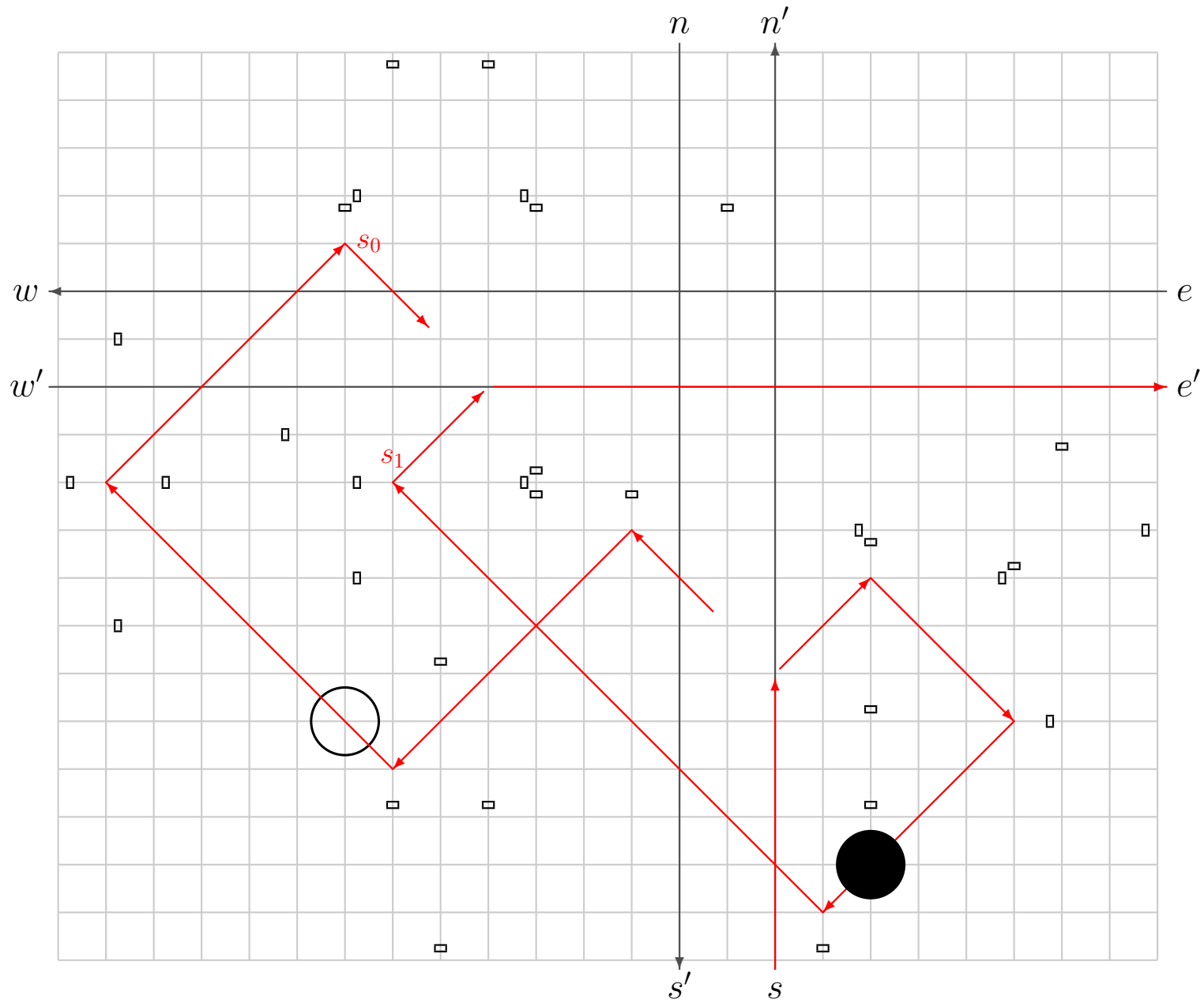




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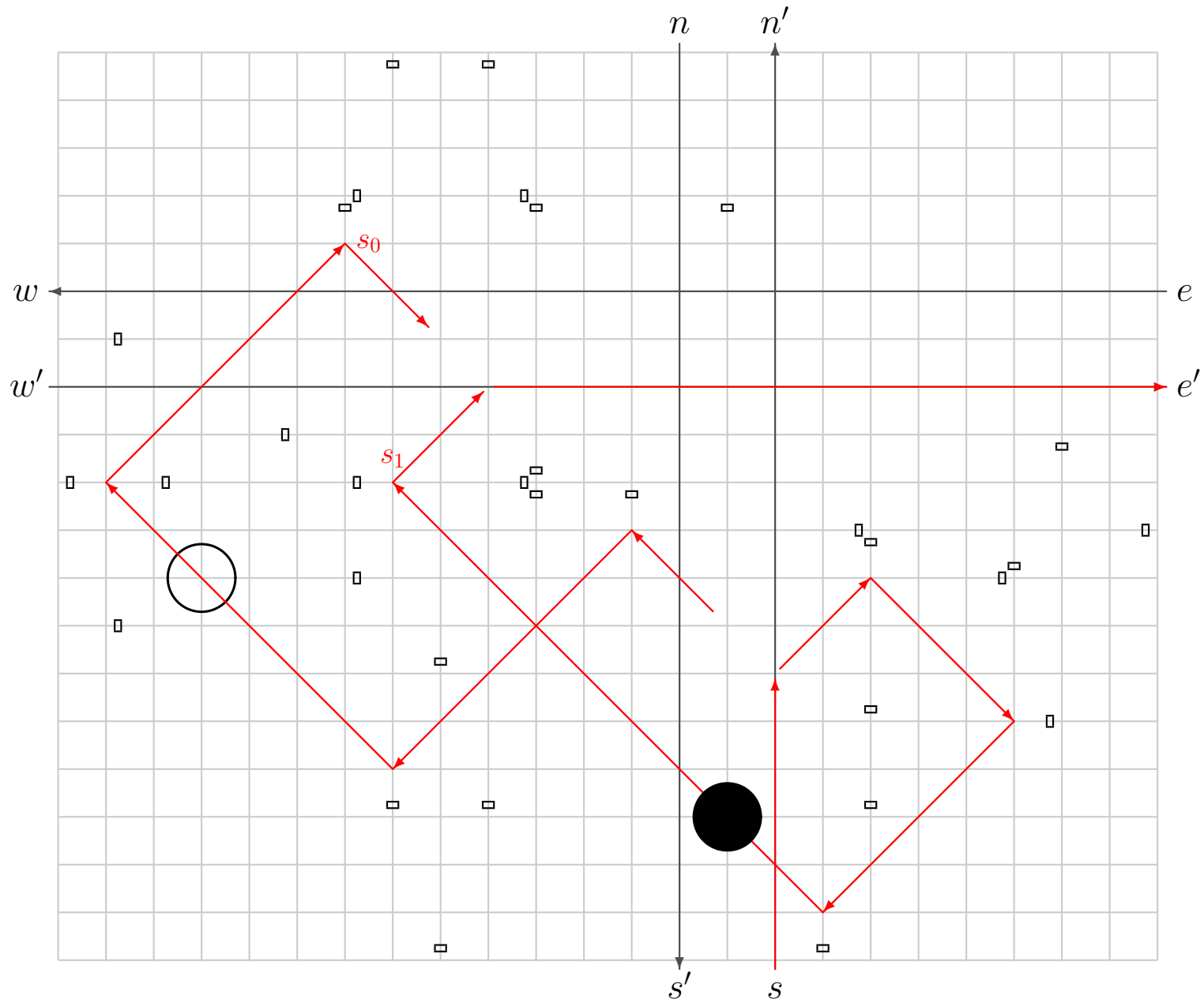
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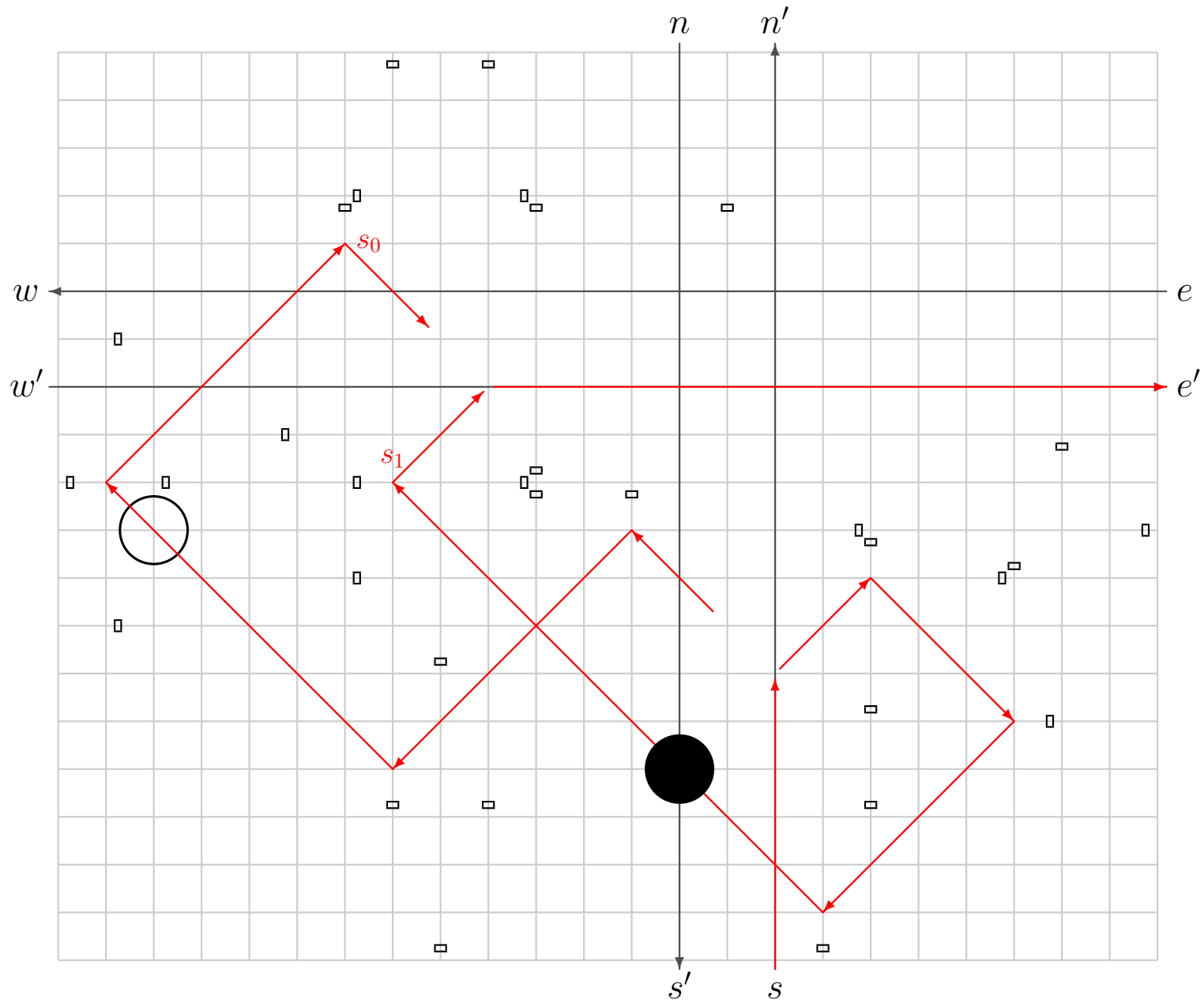




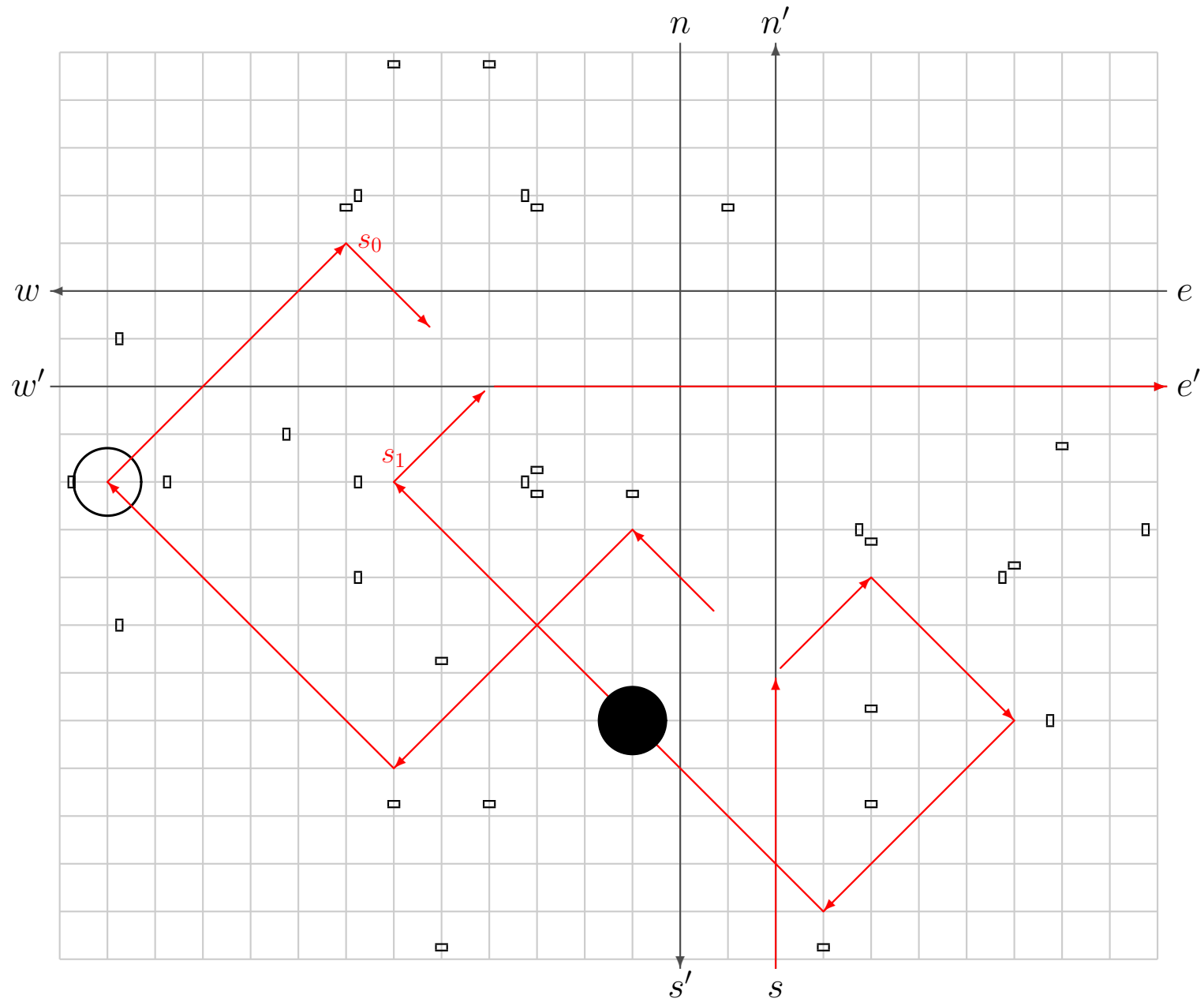
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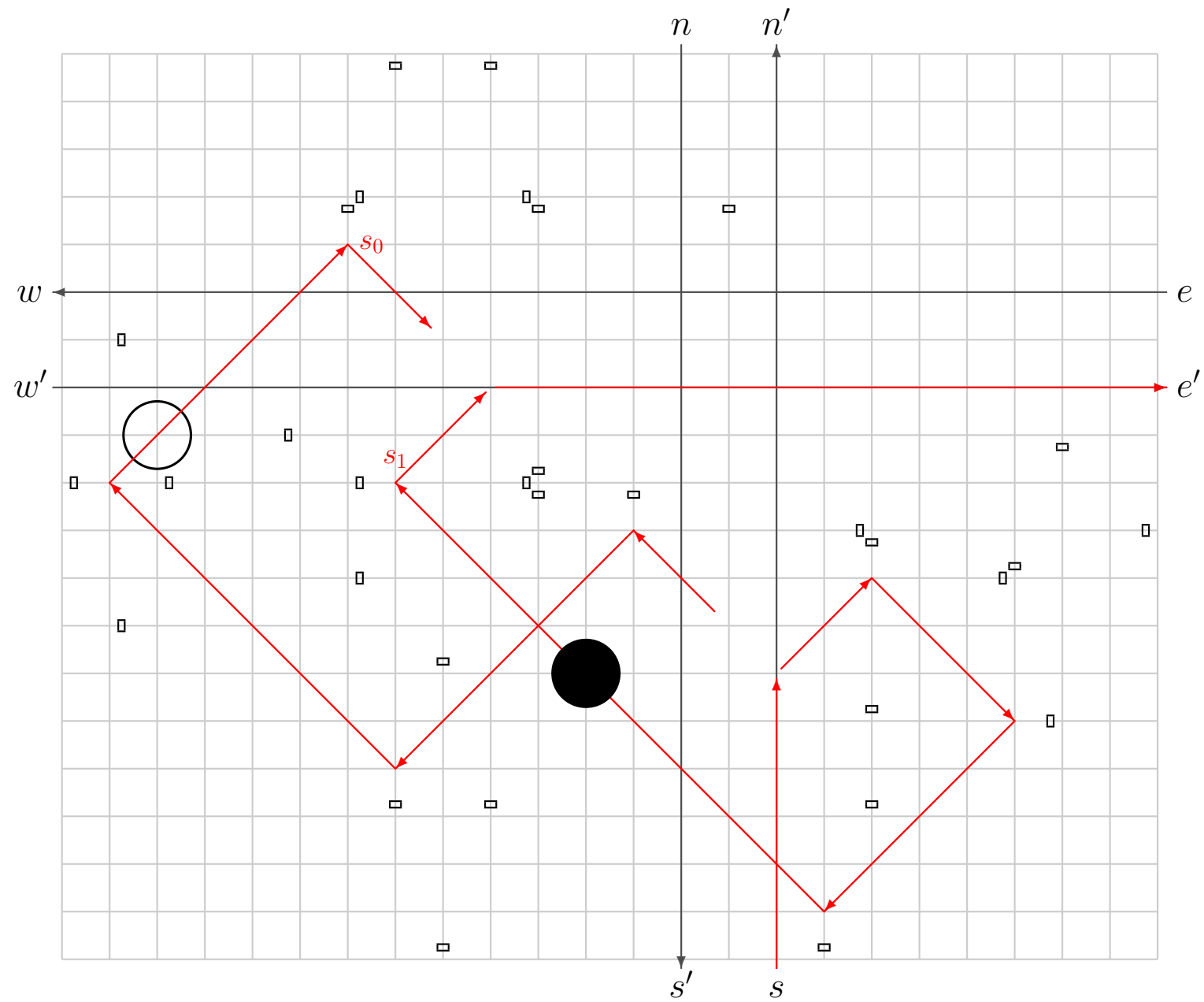
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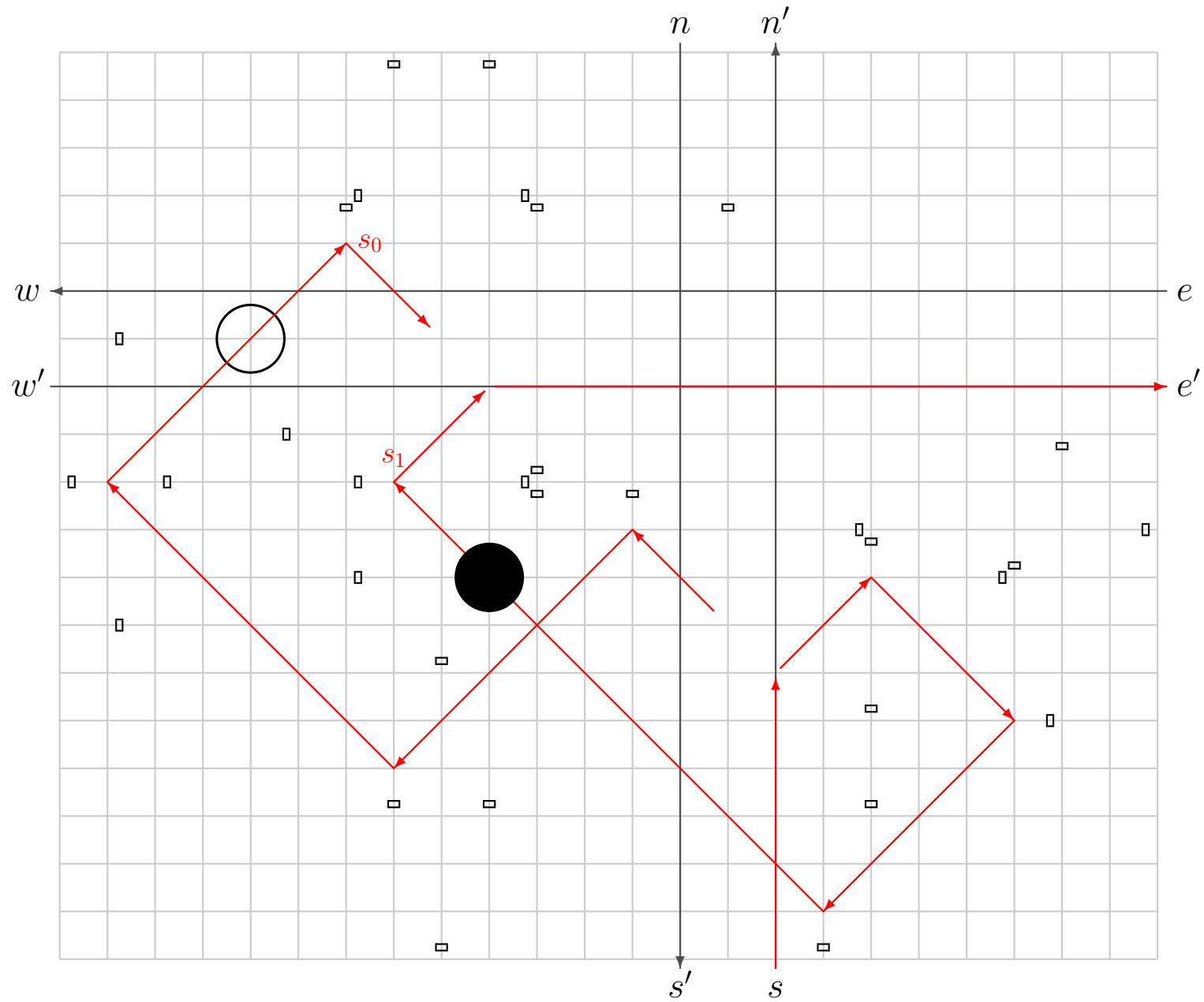
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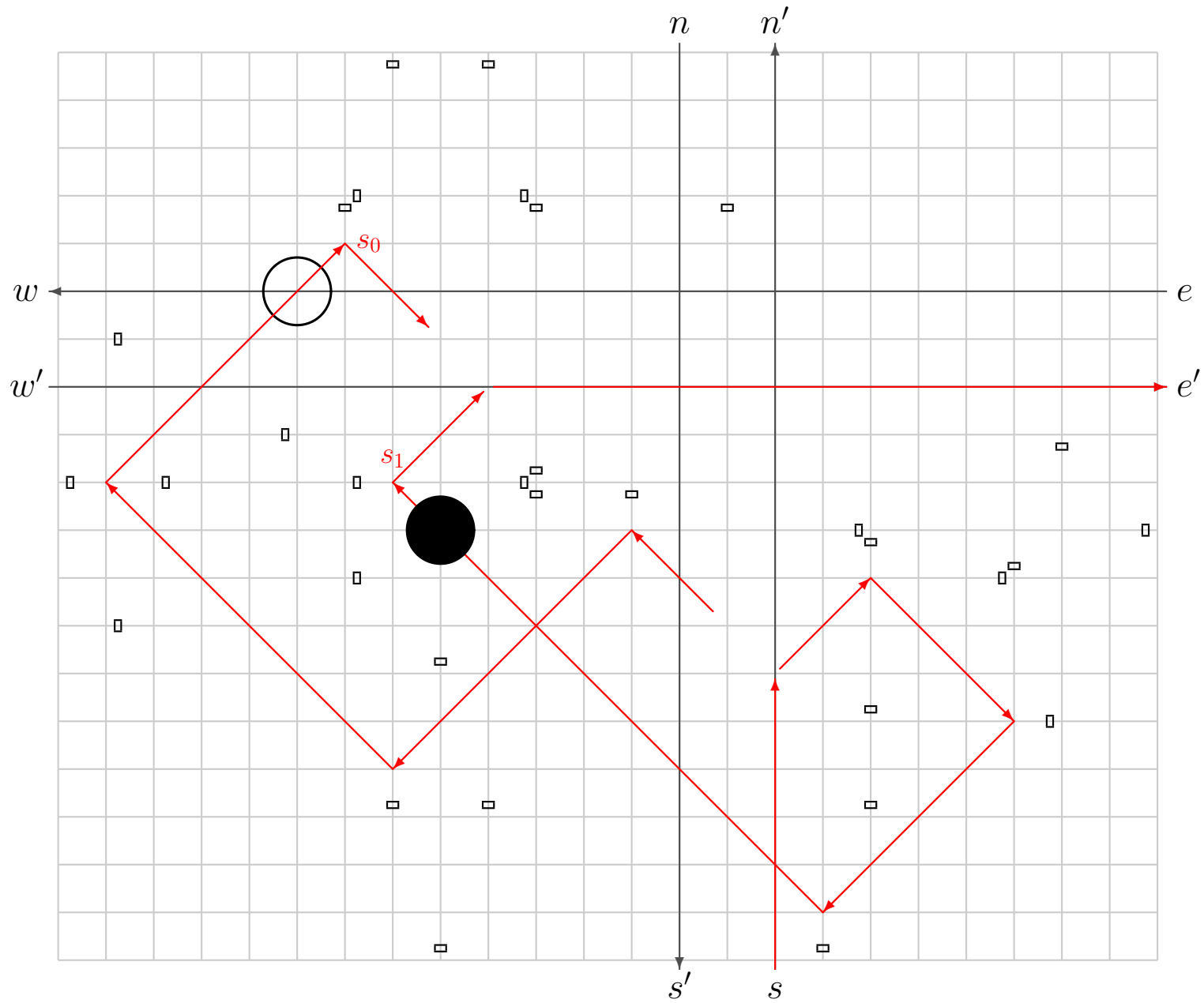




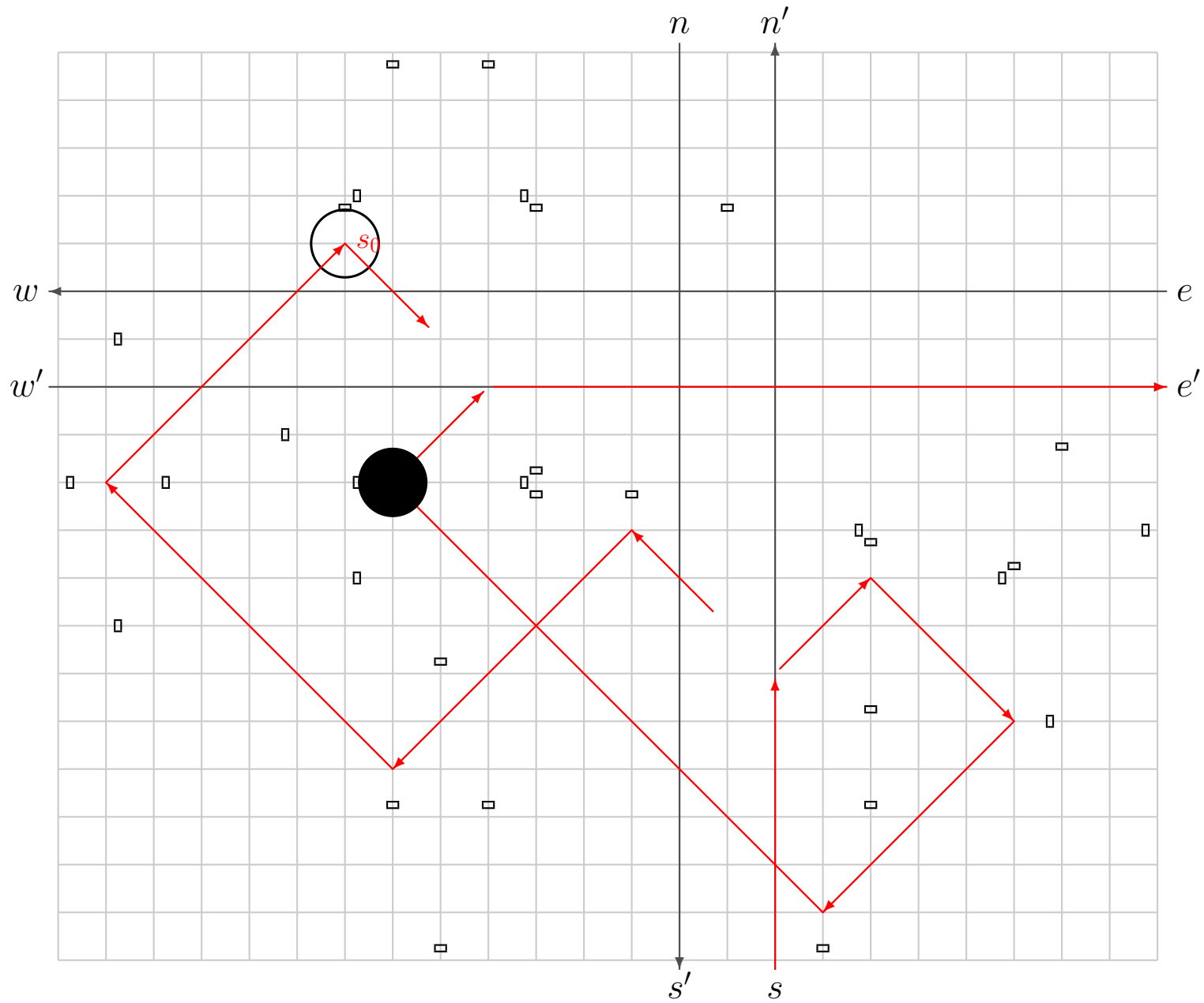
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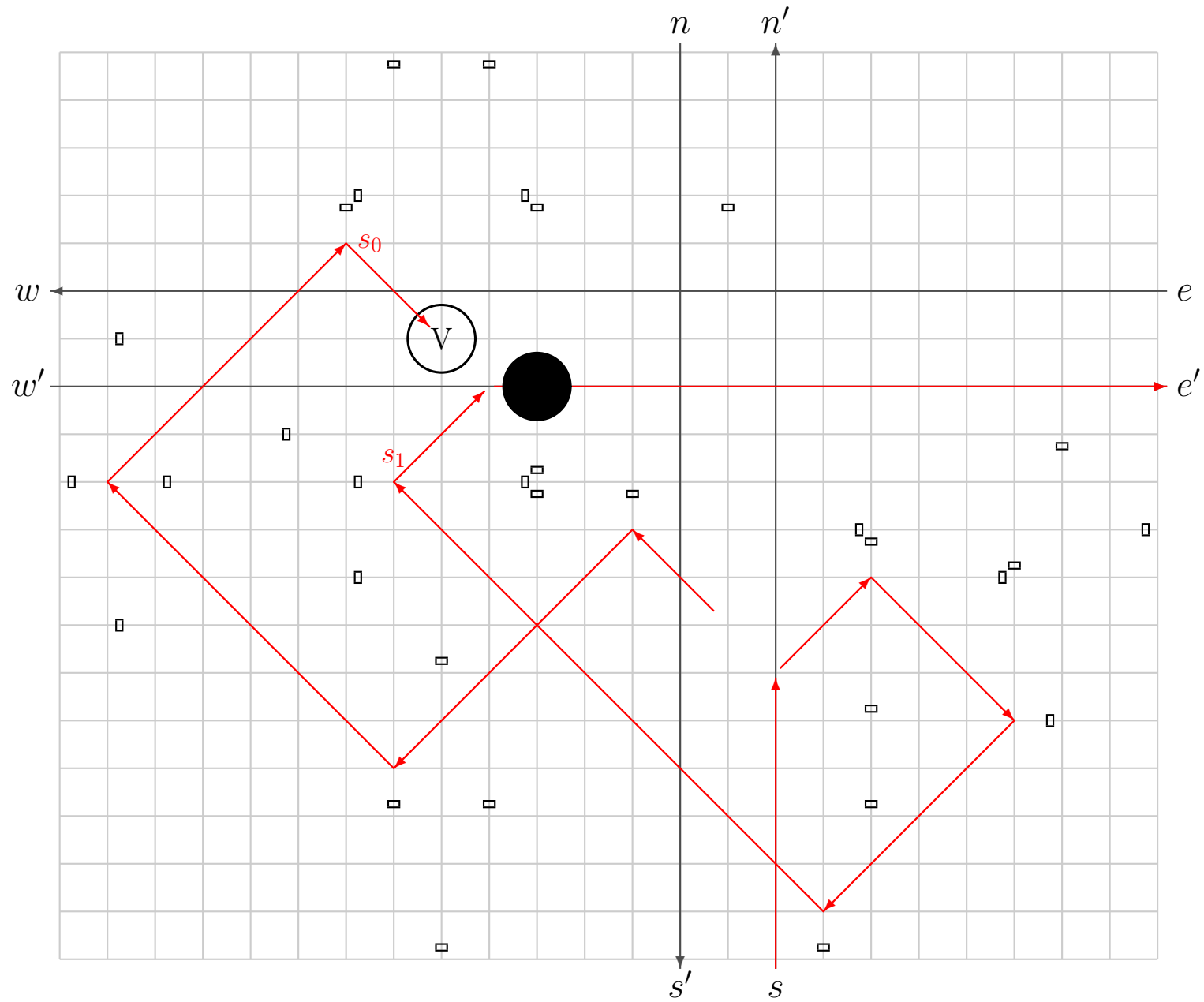
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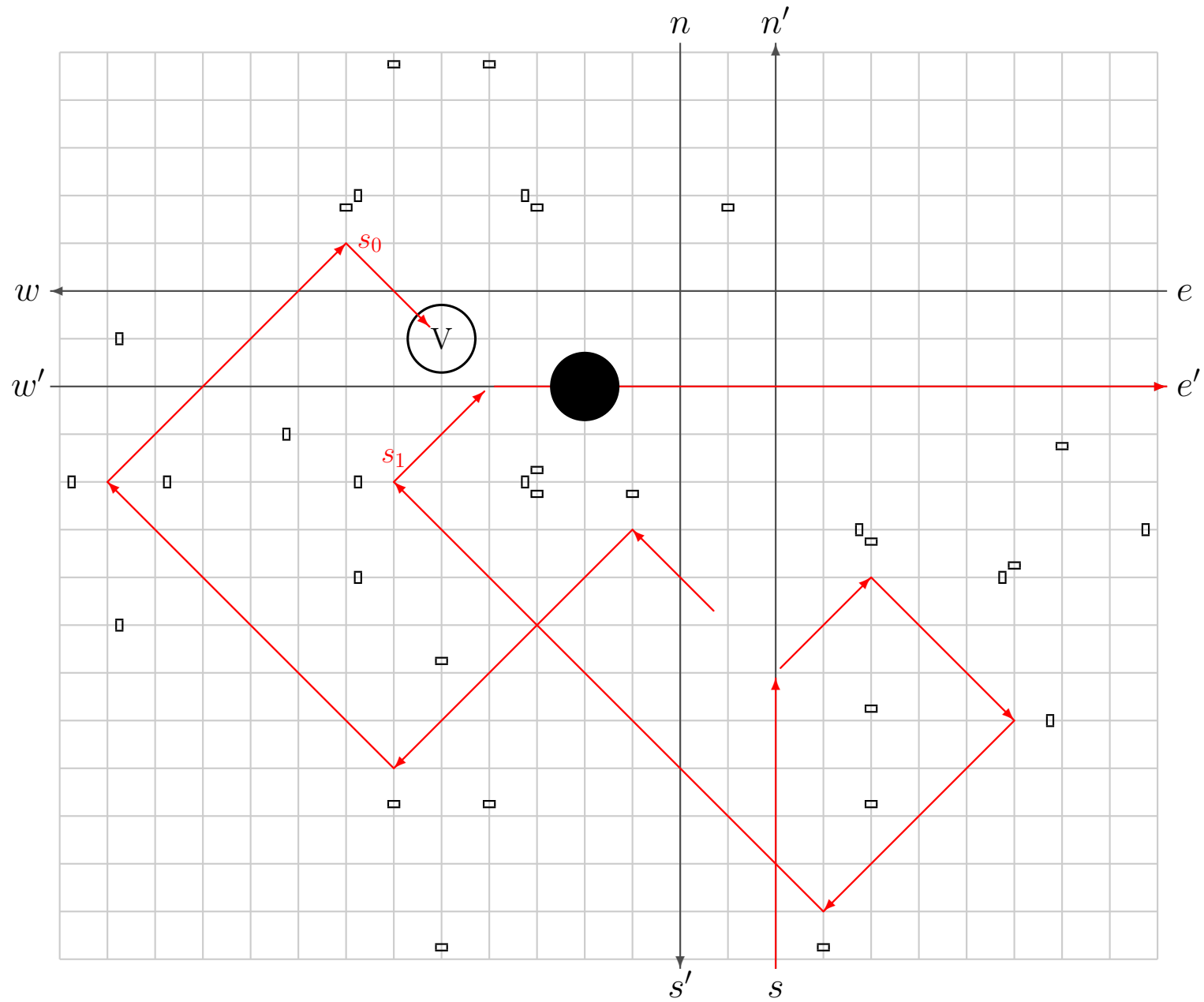




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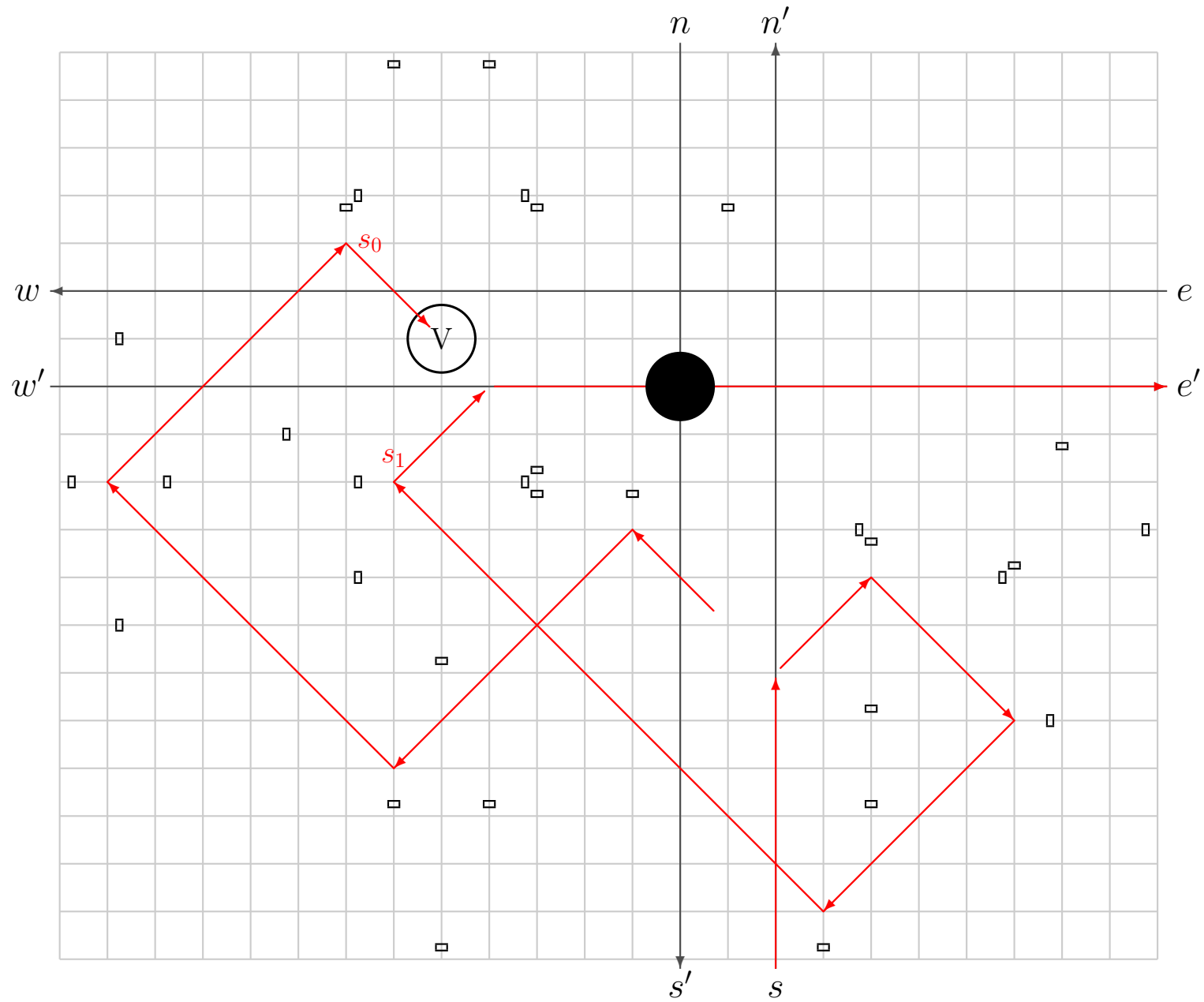
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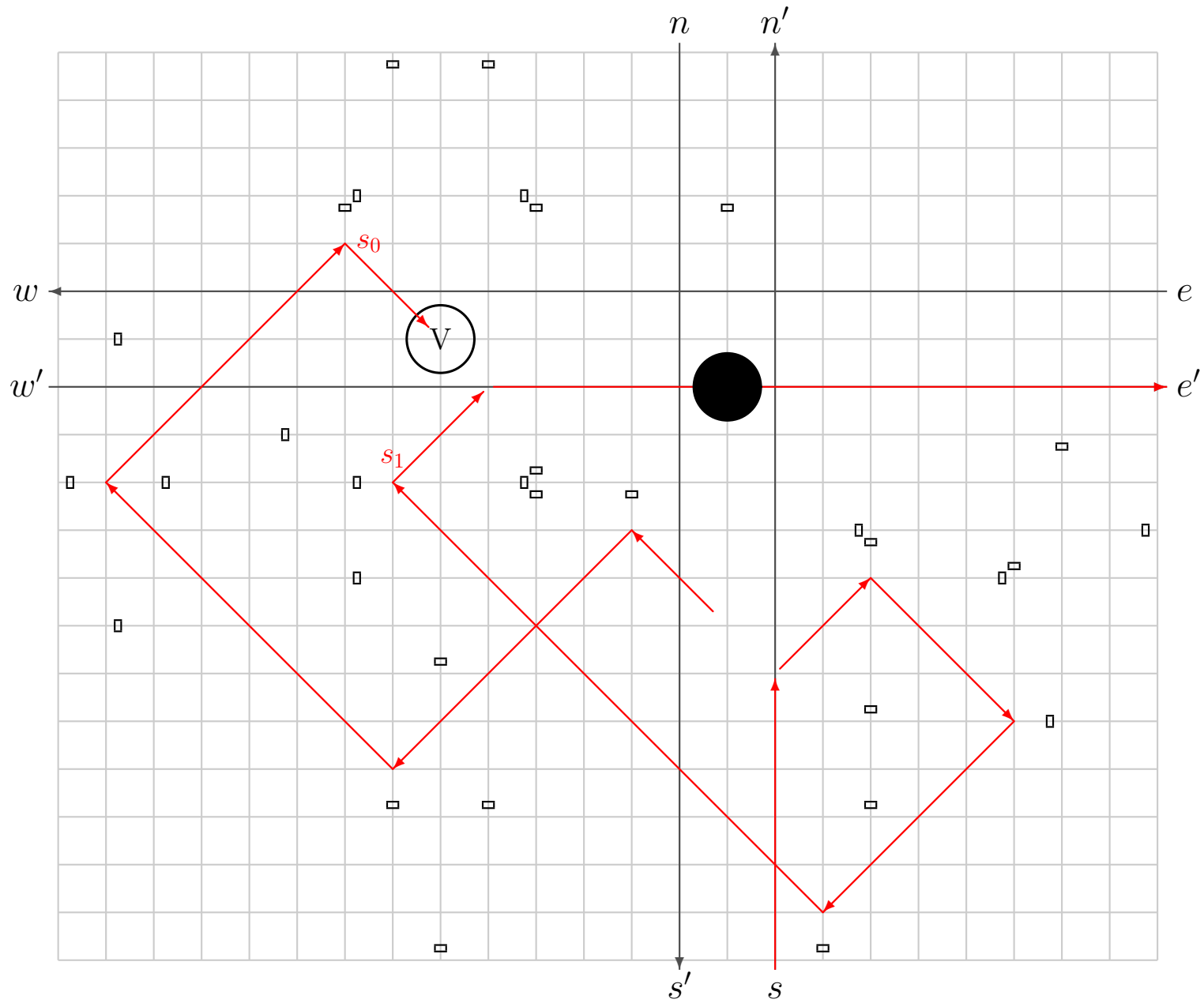




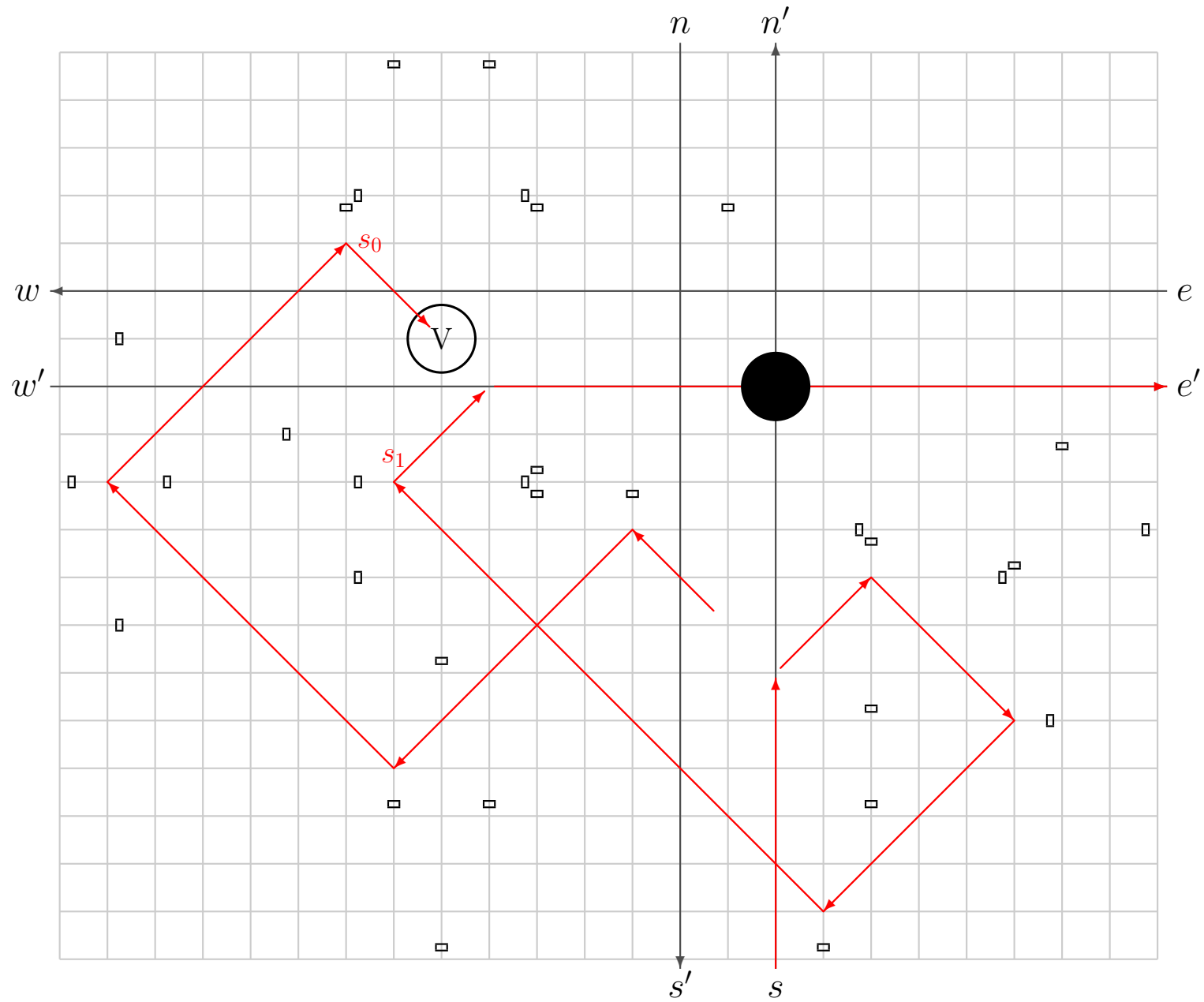
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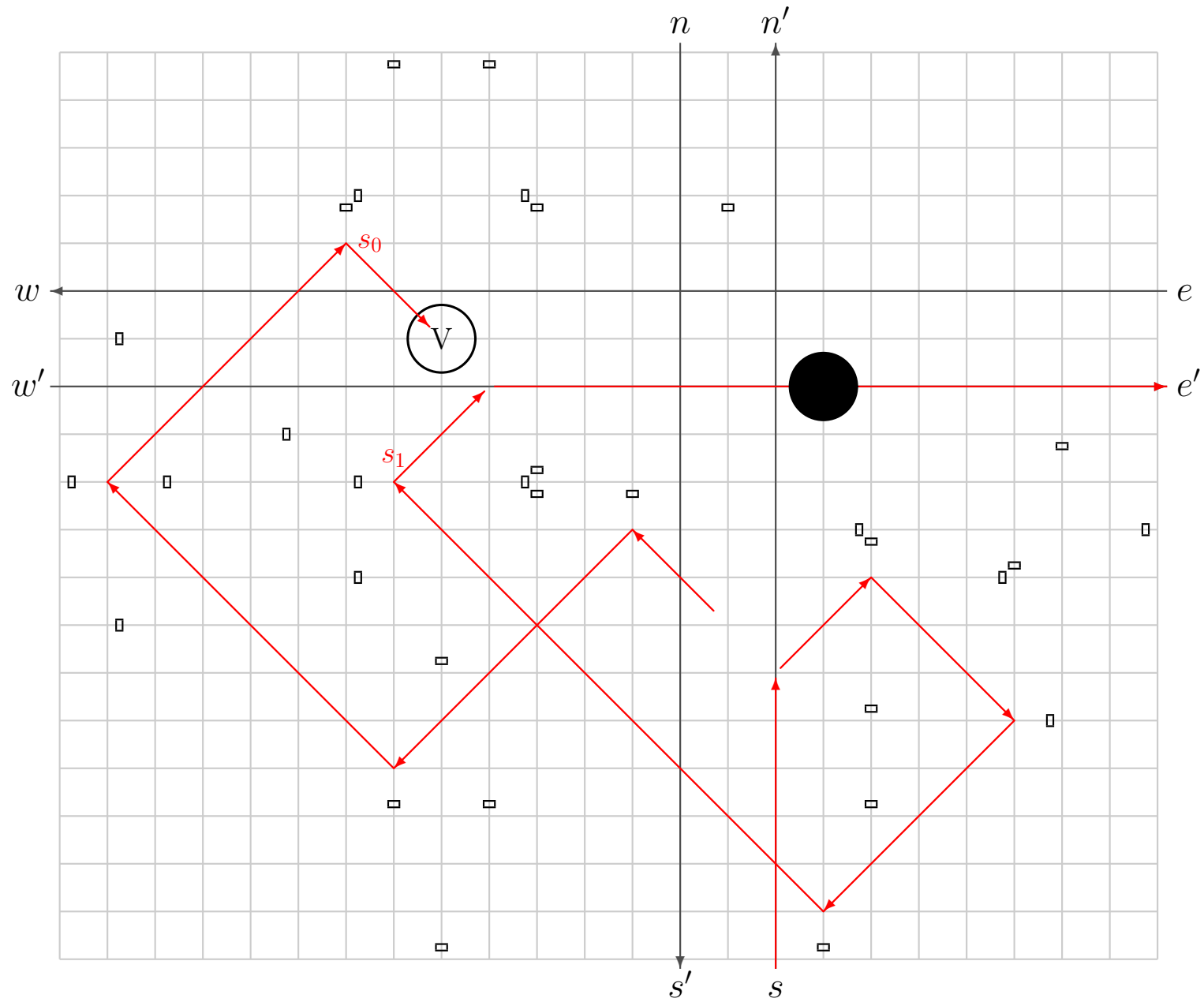
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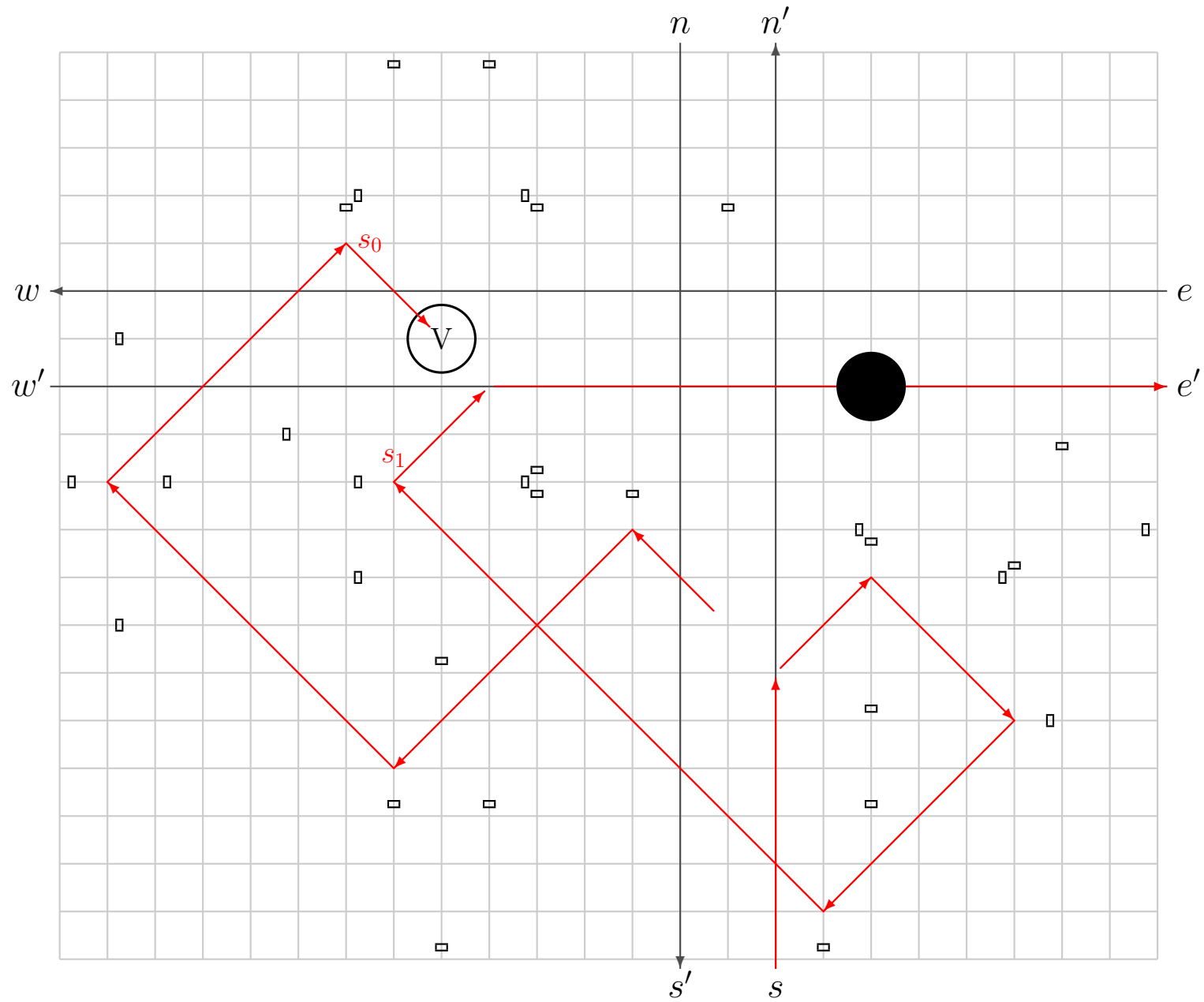
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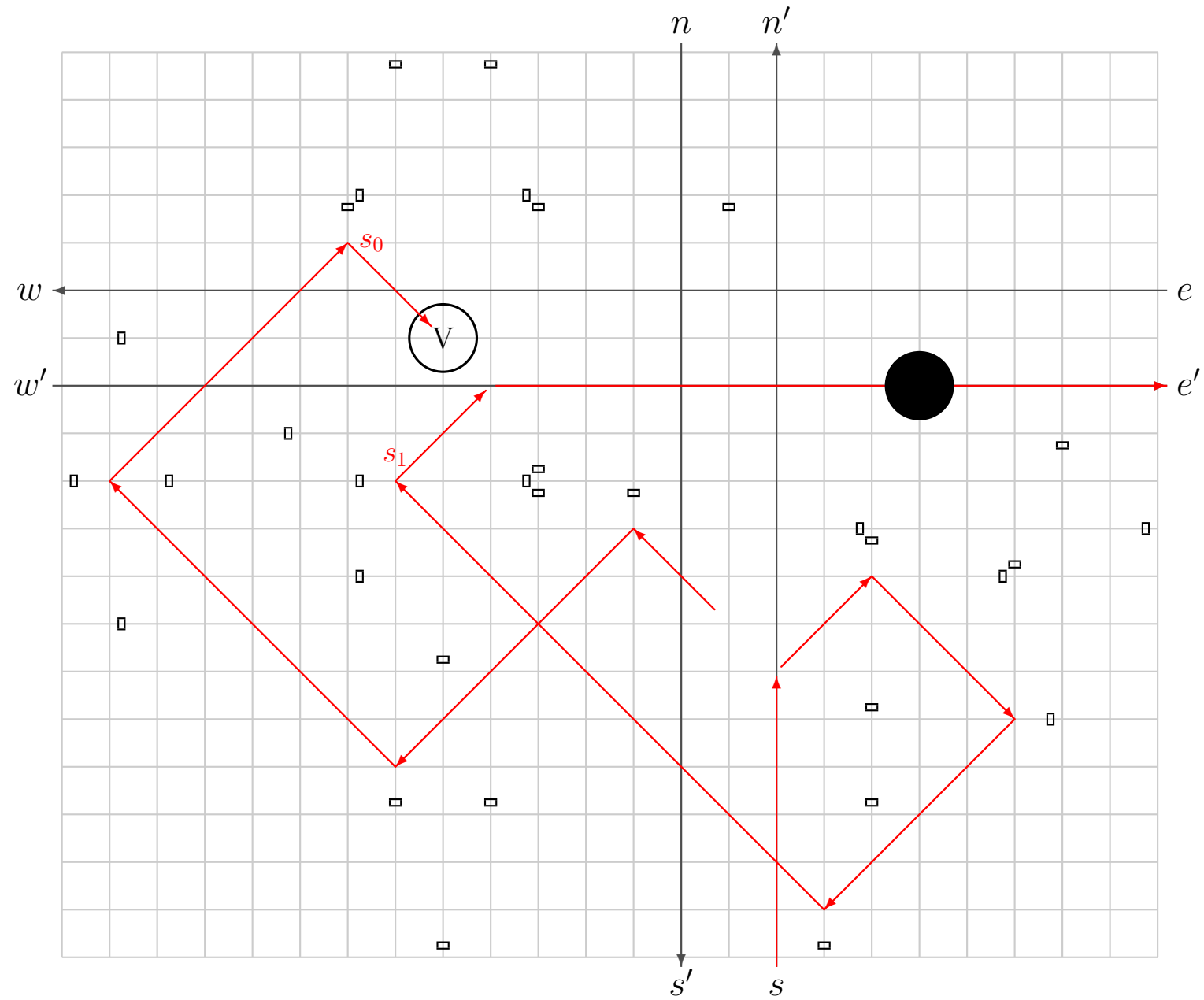
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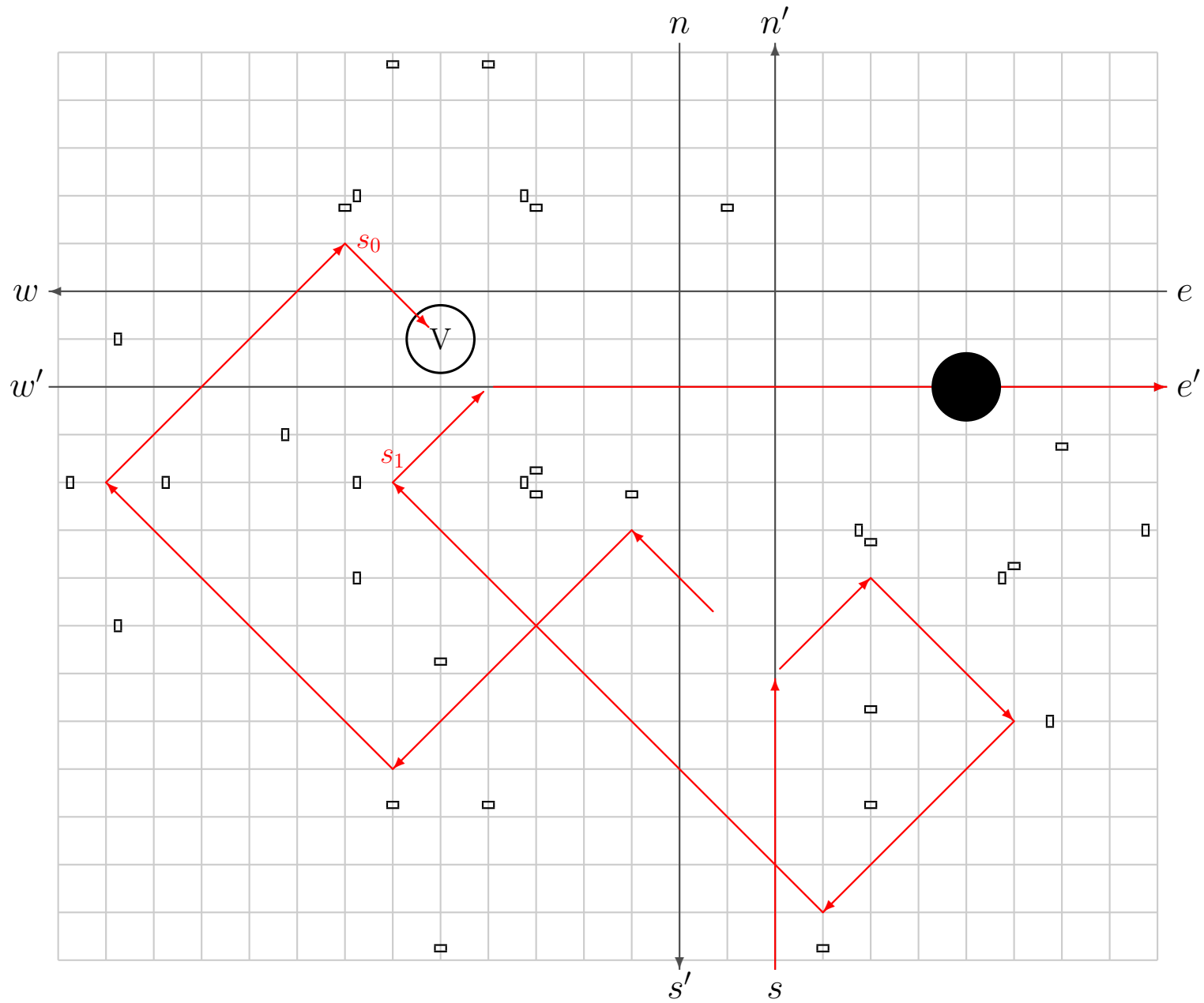
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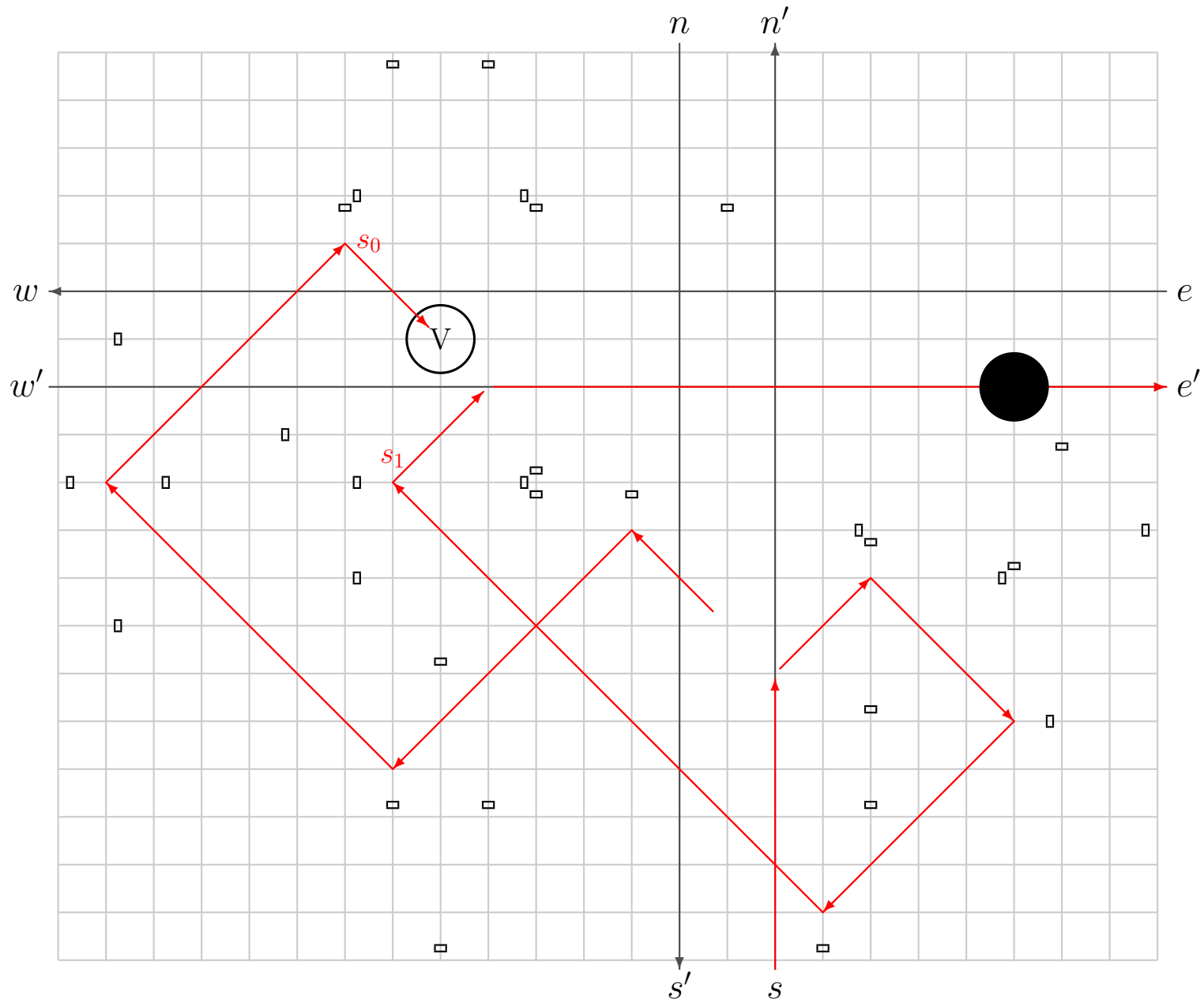


# Movements of Balls (State: $H$ , Input: $s$ )

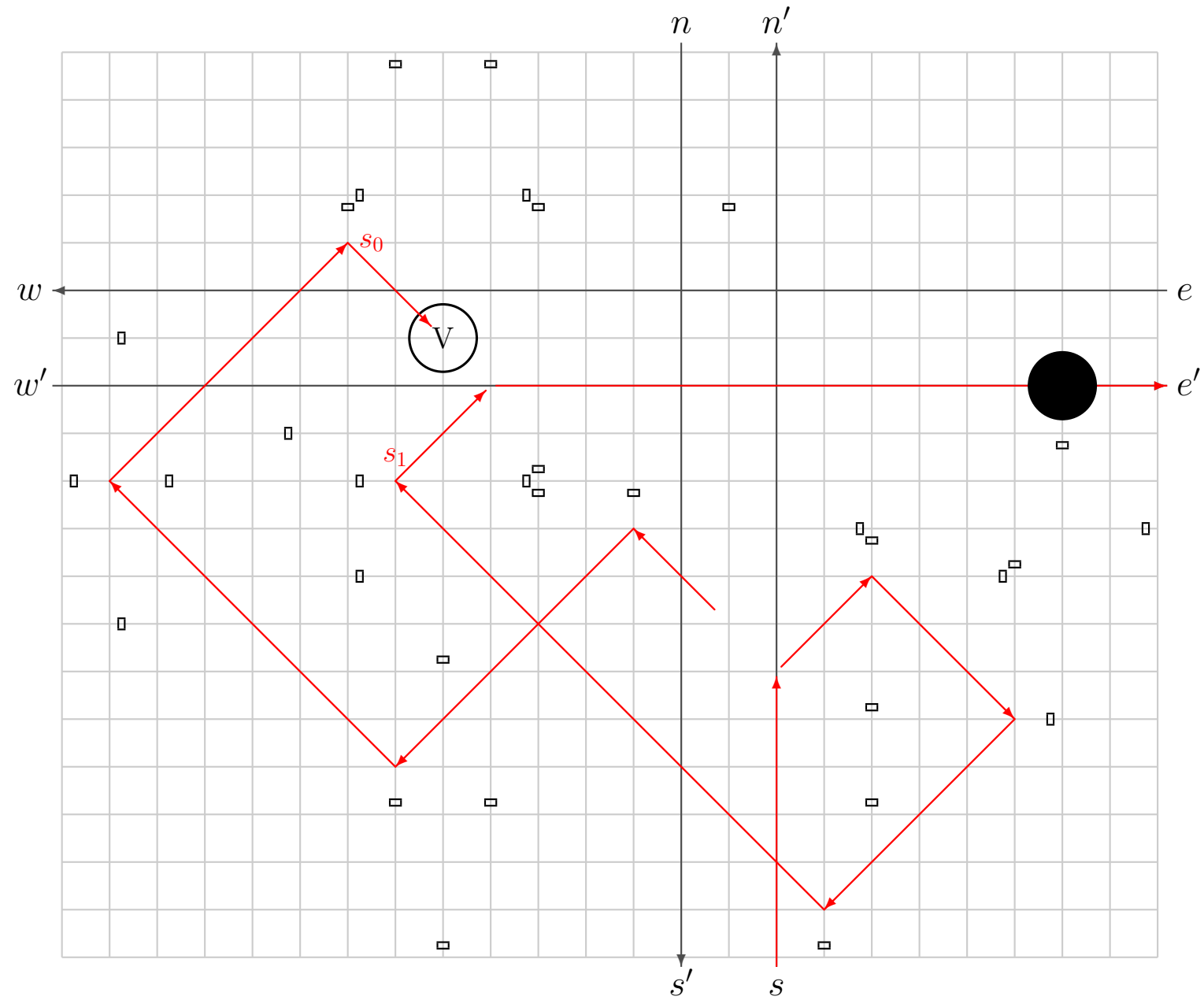




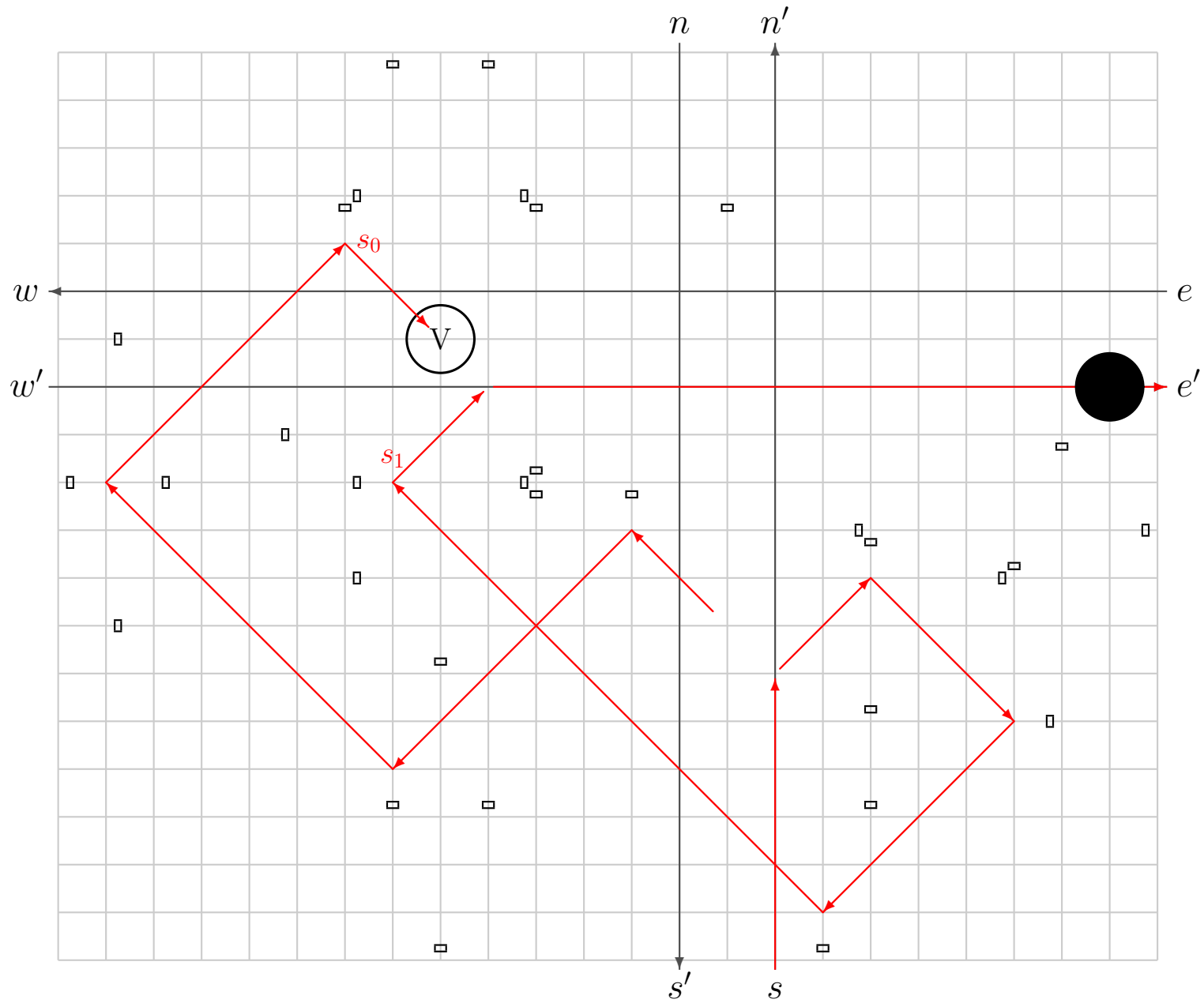
# Movements of Balls (State: $H$ , Input: $s$ )



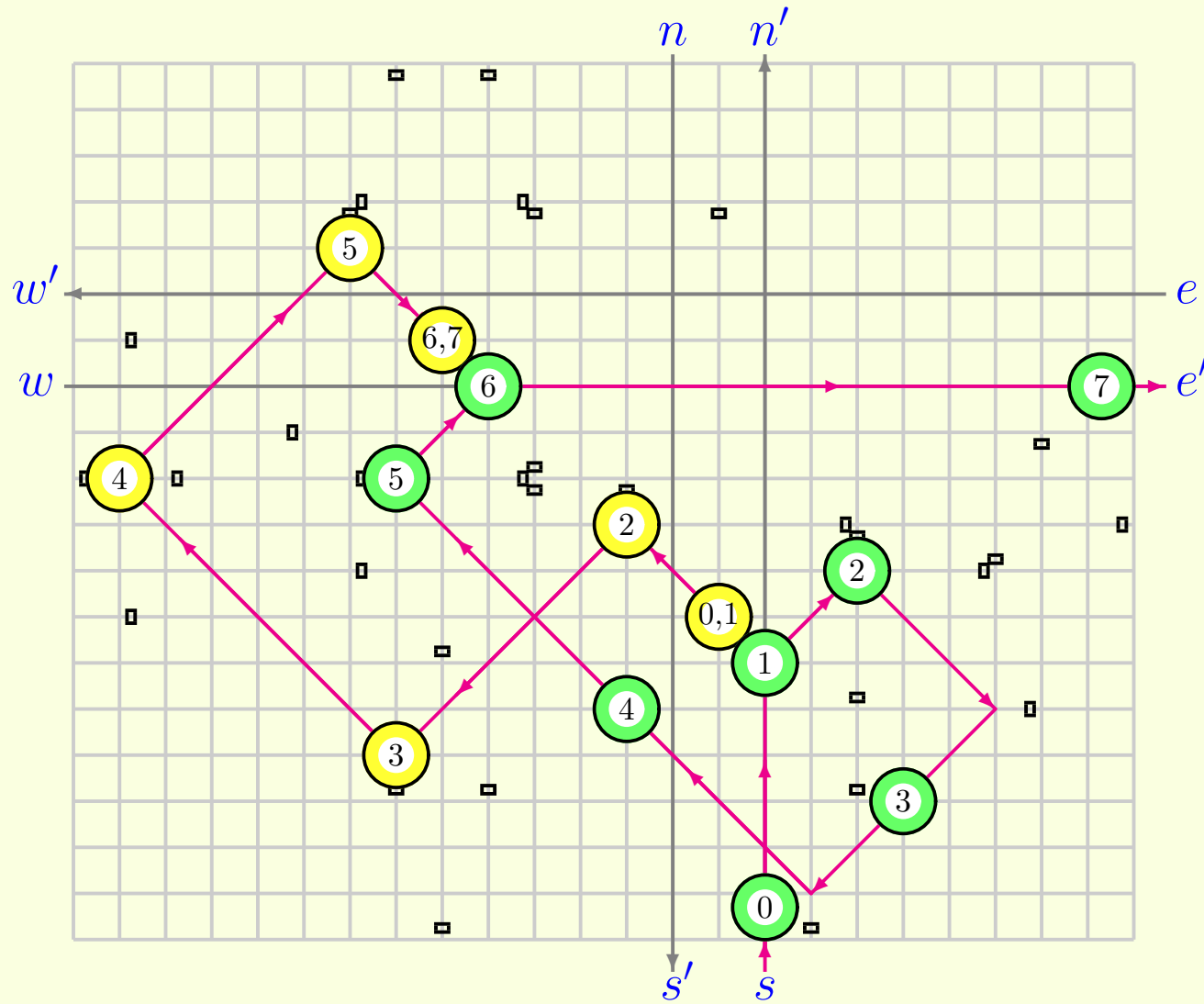
# Movements of Balls (State: $H$ , Input: $s$ )



# Movements of Balls (State: $H$ , Input: $s$ )



# BBM realization of an RE (orthogonal case)



**4. Which RLEM is universal, and which is not?**

## 4. Which RLEM is universal, and which is not?

### Answer:

- There are infinitely many 2-state RLEMs, and “all” of them except only 4 are universal.

## RE is a 2-state 4-symbol RLEM

$$M_{RE} = ( \underbrace{\{ \square_{\cdot}, \square_{\cdot} \}}_{\text{internal states}}, \underbrace{\{n, e, s, w\}}_{\text{input symbols}}, \underbrace{\{n', e', s', w'\}}_{\text{output symbols}}, \underbrace{\delta_{RE}}_{\text{move function}} )$$

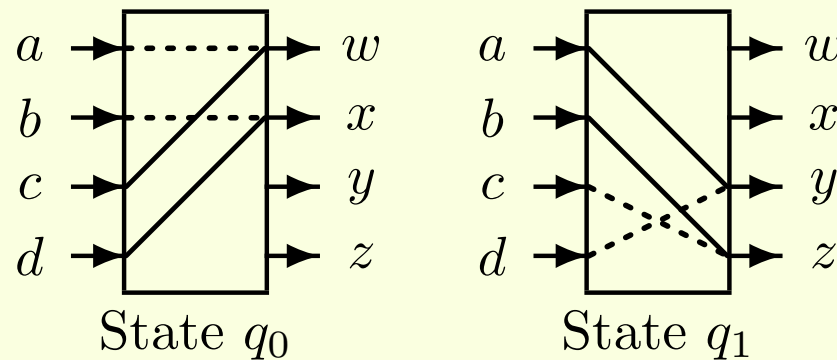
The move function  $\delta_{RE}$ :

Present state	Input			
	$n$	$e$	$s$	$w$
State H: $\square_{\cdot}$	$\square_{\cdot} w'$	$\square_{\cdot} w'$	$\square_{\cdot} e'$	$\square_{\cdot} e'$
State V: $\square_{\cdot}$	$\square_{\cdot} s'$	$\square_{\cdot} n'$	$\square_{\cdot} n'$	$\square_{\cdot} s'$

# We represent a 2-state RLEM graphically

Example: RLEM 4-289 (equivalent to an RE)

Present state	Input			
	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>
State $q_0$	$q_0 w$	$q_0 x$	$q_1 w$	$q_1 x$
State $q_1$	$q_0 y$	$q_0 z$	$q_1 z$	$q_1 y$



Solid edge: the state **changes to another**  
 Dotted edge: the state **remains unchanged**



## 2-state $k$ -symbol RLEMs ( $k$ -RLEMs)

1. Total numbers of RLEMs:

- 2-state 2-symbol RLEMs:  $4! = 24$
- 2-state 3-symbol RLEMs:  $6! = 720$
- 2-state 4-symbol RLEMs:  $8! = 40320$

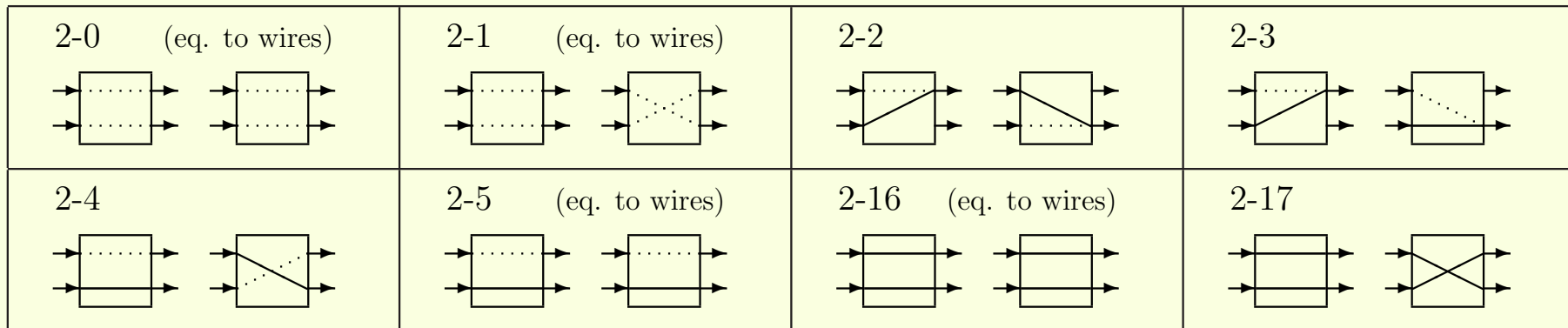
ID numbers are given to RLEMs:

e.g., No.2-17, No.3-453, etc.

2. Equivalence classes under the permutation of states and I/O symbols:

- 2-state 2-symbol RLEMs: 8 classes
- 2-state 3-symbol RLEMs: 24 classes
- 2-state 4-symbol RLEMs: 82 classes

## 8 representatives of 2-RLEMs



# 24 Representatives of 3-RLEMs

<p>3-0 (eq. to wires)</p>	<p>3-1 (eq. to wires)</p>	<p>3-3 (eq. to wires)</p>	<p>3-6 (eq. to 2-2)</p>
<p>3-7</p>	<p>3-9</p>	<p>3-10</p>	<p>3-11 (eq. to 2-3)</p>
<p>3-18</p>	<p>3-19 (eq. to 2-4)</p>	<p>3-21 (eq. to wires)</p>	<p>3-23</p>
<p>3-60</p>	<p>3-61</p>	<p>3-63</p>	<p>3-64</p>
<p>3-65</p>	<p>3-90</p>	<p>3-91</p>	<p>3-94 (eq. to wires)</p>
<p>3-95 (eq. to 2-17)</p>	<p>3-450 (eq. to wires)</p>	<p>3-451</p>	<p>3-453</p>

# 82 Representatives of 4-RLEMs

4-0 (eq. to wires)	4-1 (eq. to wires)	4-3 (eq. to wires)	4-7 (eq. to wires)	4-9 (eq. to wires)	4-24 (eq. to 2-2)
4-25 (eq. to 3-7)	4-26	4-27	4-31	4-33	4-34
4-35 (eq. to 3-9)	4-42	4-43 (eq. to 3-10)	4-45 (eq. to 2-3)	4-47	4-96
4-97 (eq. to 3-18)	4-99 (eq. to 2-4)	4-101	4-105 (eq. to wires)	4-107 (eq. to 3-27)	4-113
4-288	4-289	4-290	4-291	4-293	4-304
4-305	4-312	4-313	4-314 (eq. to 3-60)	4-315 (eq. to 3-61)	4-316
4-317	4-319	4-321 (eq. to 3-63)	4-322	4-323	4-328
4-329	4-330	4-331	4-333	4-334 (eq. to 3-64)	4-335 (eq. to 3-65)
4-576	4-577	4-578	4-579	4-580 (eq. to 3-90)	4-581 (eq. to 3-91)
4-592 (eq. to wires)	4-593 (eq. to 2-17)	4-598	4-599	4-2592	4-2593
4-2594	4-2595	4-2596	4-2597	4-2608	4-2609
4-2610	4-2611	4-2614	4-2615	4-3456	4-3457
4-3460	4-3461	4-3474 (eq. to wires)	4-3475 (eq. to 3-451)	4-3477 (eq. to 3-453)	4-23616 (eq. to wires)
4-23617	4-23619	4-23623	4-23625		

# Numbers of representatives of degenerate and non-degenerate 2-, 3-, and 4-RLEMs

$k$	Total	Degenerate $k$ -RLEMs			Non-degenerate $k$ -RLEMs
		Type (i)	Type (ii)	Type (iii)	
2	8	2	2	0	4
3	24	3	3	4	14
4	82	5	4	18	55

## Universality of an RE

From the previous discussion, we can see that an RE is universal in the following sense.

- Any reversible sequential machine can be realized by a garbage-less circuit made only of REs.
- Any reversible Turing machine can be realized by a garbage-less infinite circuit made of REs.

On the other hand, there are many kinds of RLEMs, and thus we have a question.

- Which RLEM is universal and which is not?

**Non-degenerate  $k$ -RLEMs are ALL universal  
if  $k > 2$**

**Lemma 1** [Lee et al., 2008] An RE is simulated by a circuit composed of RLEMs 2-3 and 2-4.

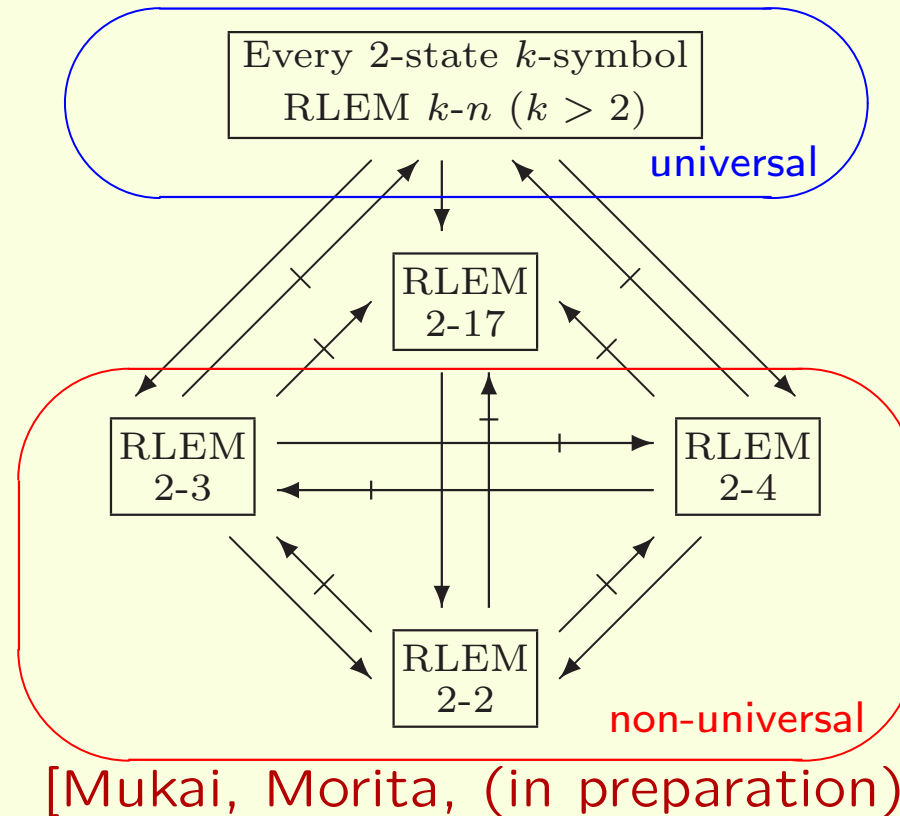
**Lemma 2** RLEMs 2-3 and 2-4 can be constructed by any one of 14 non-degenerate 3-RLEMs.

**Lemma 3** Any non-degenerate  $k$ -RLEM can simulate a non-degenerate  $(k - 1)$ -RLEM, if  $k > 2$ .

The next theorem is derived from Lemmas 1–3.

**Theorem** [Morita et al., 2010] Every non-degenerate  $k$ -RLEM is universal, if  $k > 2$ .

# Hierarchy of 2-state non-degenerate RLEMs



- $A \rightarrow B$  ( $A \not\rightarrow B$ , respectively) represents that  $A$  can (cannot) simulate  $B$ .
- Any two combination of  $\{2-3, 2-4, 2-17\}$  is universal. [Lee, et al., 2008], [Mukai, Morita, (in preparation)]



## Concluding Remarks

Q1. What are RLEMs?

A: They look interesting objects to study.

Q2. How can we construct reversible machines by RLEMs?

A: There is an easy implementation method.

Q3. Can RLEMs be implemented in a reversible physical system efficiently?

A: Ideally yes, but practically unknown.

Q4. Which RLEM is universal, and which is not?

A: There are several simple and universal RLEMs.

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